# addon

## HCD00D40C0000-0-AO

Fujitsu<sup>®</sup> HCD00D40C0000-0 Compatible TAA Compliant 100GBase-ER4 QSFP28 Transceiver (SMF, 1295nm to 1309nm, 40KM, LC, DOM)

#### Features:

- Compliant with IEEE Std 802.3ba, 100G Ethernet ER4
- Compliant with QSFP28 MSA
- 4 cooled 25Gb/s channels LAN WDM EML TOSA
- 4 channels SOA PIN photo detector
- Single +3.3V power supply
- Class 1 laser safety certified
- Operating temperature: 0°C to +70°C
- Up to 40km on SMF without FEC
- Duplex LC connector
- RoHS 6/6 Compliant

#### Applications

- 100GBASE-ER4 Ethernet links
- Data center

## **Product Description**

This Fujitsu<sup>®</sup> HCD00D40C0000-0 compatible QSFP28 transceiver provides 100GBase-ER4 throughput up to 40km over single-mode fiber (SMF) using a wavelength of 1295nm to 1309nm via an LC connector. It is guaranteed to be 100% compatible with the equivalent Fujitsu<sup>®</sup> transceiver. This easy to install, hot swappable transceiver has been programmed, uniquely serialized and data-traffic and application tested to ensure that it will initialize and perform identically. Digital optical monitoring (DOM) support is also present to allow access to real-time operating parameters. This transceiver is Trade Agreements Act (TAA) compliant. We stand behind the quality of our products and proudly offer a limited lifetime warranty.

AddOn's transceivers are RoHS compliant and lead-free.



TAA refers to the Trade Agreements Act (19 U.S.C. & 2501-2581), which is intended to foster fair and open international trade. TAA requires that the U.S. Government may acquire only "U.S. – made or designated country end products."

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## **Regulatory Compliance**

- ESD to the Electrical PINs: compatible with MIL-STD-883E Method 3015.4
- ESD to the LC Receptacle: compatible with IEC 61000-4-3
- EMI/EMC compatible with FCC Part 15 Subpart B Rules, EN55022:2010
- Laser Eye Safety compatible with FDA 21CFR, EN60950-1& EN (IEC) 60825-1,2
- RoHS compliant with EU RoHS 2.0 directive 2015/863/EU

#### **Absolute Maximum Ratings**

Parameter	Symbol	Min.	Тур.	Max.	Unit
Storage Temperature	Ts	-40		85	°C
Operating Case Temperature	T <sub>op</sub>	0	25	70	°C
Power Supply Voltage	Vcc	-0.5		4.0	V
Relative Humidity	RH	5		95	%
Data Rate PER Channel			25.78125		Gb/s

## **Electrical Characteristics**

Parameter	Symbol	Min	Тур	Max	Unit	Notes	
Supply Voltage	Vcc	3.135	3.3	3.465	V		
Module Supply Current	lcc			TBD	mA		
Power Dissipation	PD			TBD	mW		
Transmitter							
Single-ended Input Voltage Tolerance		-0.3		4.0	V		
Input Differential Impedance	ZIN		100		Ω		
Differential Data Input Swing	VIN, P-P	190		700	mVP-P		
AC Common Mode Input Voltage Tolerance		15			mV		
Differential Input Voltage Swing Threshold			50		mVpp		
Receiver							
Single-ended Output Voltage		-0.3		4.0	V		
Output Differential Impedance	ZO	90	100	110	Ω		
Differential Data Output Swing	VOUT, P-P	300		850	mVP-P		
AC Common Mode Output Voltage				7.5	mV		

## **Optical Characteristics**

Parameter	Symbol	Min	Typical	Max	Unit	Notes		
Transmitter								
Launch Optical Power per lane	Ро	-2.9		+2.9	dBm	1		
Total Launch Optical Power	Ро			+8.9	dBm	1		
Center Wavelength Range	L1	1294.53	1295.56	1296.59	nm			
	L2	1299.02	1300.05	1301.09	nm			
	L3	1303.54	1304.58	1305.63	nm			
	L4	1308.09	1309.14	1310.19	nm			
Extinction Ratio	EX	8.0			dB	2		
Spectral width(-20dB)	Δλ			1	nm			
Side Mode Suppression Ratio	SMSR	30			dB			
Optical Return Loss Tolerance	ORLT			20	dB			
Pout @TX-Disable Asserted	Poff			-30	dBm	1		
Eye Mask {X1, X2, X3, Y1, Y2, Y3}		{0.25, 0.4, 0.45, 0.25, 0.28, 0.4}						
Receiver	Receiver							
Center Wavelength	L1	1294.53	1295.56	1296.59	nm			
	L2	1299.02	1300.05	1301.09	nm			
	L3	1303.54	1304.58	1305.63	nm			
	L4	1308.09	1309.14	1310.19	nm			
Sensitivity per Channel (OMA)	S			-20.5	dBm	1		
Overload (each channel)	POL	-7.0			dBm	1		
Damage Threshold (each channel)	Pdamage	-6.0			dBm			
Receiver Reflectance	Rf			-26	dB			
LOS De-Assert	LOSD			-21.0	dBm			
LOS Assert	LOSA	-26.0			dBm			
LOS Hysteresis		0.5			dB			

## Notes:

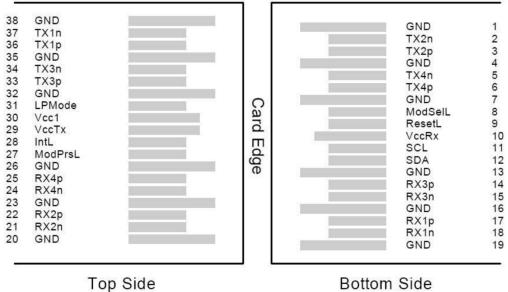
- 1. The optical power is launched into SMF.
- Measured with a PRBS 2<sup>31</sup>-1 test pattern @25.78125Gbps.
  Measured with PRBS 2<sup>31</sup>-1 test pattern, 25.78125Gb/s, BER 1.0E-12

Pin	escriptions Logic	Symbol	Name/Descriptions	Ref.
1		GND	Module Ground	1
2	CML-I	Tx2-	Transmitter inverted data input	
3	CML-I	Tx2+	Transmitter non-inverted data input	
4		GND	Module Ground	1
5	CML-I	Tx4-	Transmitter inverted data input	
6	CML-I	Tx4+	Transmitter non-inverted data input	
7		GND	Module Ground	1
8	LVTTL-I	MODSEIL	Module Select	2
9	LVTTL-I	ResetL	Module Reset	2
10		VCCRx	+3.3v Receiver Power Supply	
11	LVCMOS-I	SCL	2-wire Serial interface clock	2
12	LVCMOS-I/O	SDA	2-wire Serial interface data	2
13		GND	Module Ground	1
14	CML-O	RX3+	Receiver non-inverted data output	
15	CML-O	RX3-	Receiver inverted data output	
16		GND	Module Ground	1
17	CML-O	RX1+	Receiver non-inverted data output	
18	CML-O	RX1-	Receiver inverted data output	
19		GND	Module Ground	1
20		GND	Module Ground	1
21	CML-O	RX2-	Receiver inverted data output	
22	CML-O	RX2+	Receiver non-inverted data output	
23		GND	Module Ground	1
24	CML-O	RX4-	Receiver inverted data output	
25	CML-O	RX4+	Receiver non-inverted data output	
26		GND	Module Ground	1
27	LVTTL-O	ModPrsL	Module Present, internal pulled down to GND	
28	LVTTL-O	IntL	Interrupt output, should be pulled up on host board	2
29		VCCTx	+3.3v Transmitter Power Supply	
30		VCC1	+3.3v Power Supply	
31	LVTTL-I	LPMode	Low Power Mode	2
32		GND	Module Ground	1
33	CML-I	Tx3+	Transmitter non-inverted data input	
34	CML-I	Tx3-	Transmitter inverted data input	
35		GND	Module Ground	1
36	CML-I	Tx1+	Transmitter non-inverted data input	
37	CML-I	Tx1-	Transmitter inverted data input	
38		GND	Module Ground	1

#### Notes:

- 1. Module circuit ground is isolated from module chassis ground with in the module.
- 2. Open collector; should be pulled up with 4.7k-10k ohms on host board to a voltage between 3.15V and 3.6V.

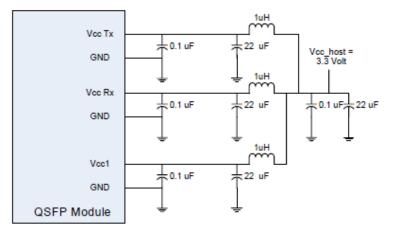
## **Electrical Pin-out Details**



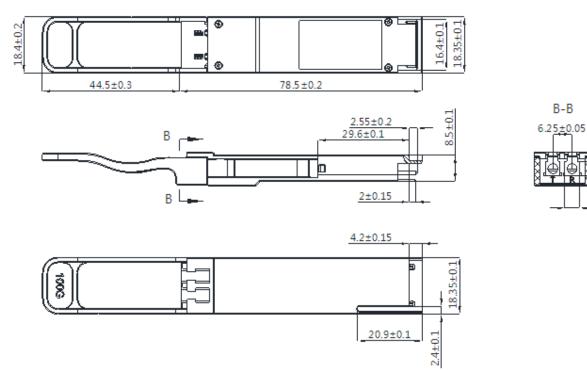
Viewed from Top

Bottom Side Viewed from Bottom

## **Recommended Host Board Power Supply Filter Network**



## **Mechanical Specifications**



 $4.7\pm0.05$ 

4.7±0.05

## **About AddOn Networks**

In 1999, AddOn Networks entered the market with a single product. Our founders fulfilled a severe shortage for compatible, cost-effective optical transceivers that compete at the same performance levels as leading OEM manufacturers. Adhering to the idea of redefining service and product quality not previously had in the fiber optic networking industry, AddOn invested resources in solution design, production, fulfillment, and global support.

Combining one of the most extensive and stringent testing processes in the industry, an exceptional free tech support center, and a consistent roll-out of innovative technologies, AddOn has continually set industry standards of quality and reliability throughout its history.

Reliability is the cornerstone of any optical fiber network and is in engrained in AddOn's DNA. It has played a key role in nurturing the long-term relationships developed over the years with customers. AddOn remains committed to exceeding industry standards with certifications from ranging from NEBS Level 3 to ISO 9001:2005 with every new development while maintaining the signature reliability of its products.

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