

# Statement of Volatility – OptiPlex Micro Plus 7020

**⚠ CAUTION: A CAUTION indicates either potential damage to hardware or erasure of data and tells you how to avoid the problem.**

The OptiPlex Micro Plus 7020 contains both volatile and non-volatile components. Volatile components erase their data immediately after power is removed from the component. Non-volatile components continue to retain their data even after power is removed from the component. The following non-volatile components are present on the OptiPlex Micro Plus 7020 system board.

**Table 1. List of non-volatile components on the system board**

Description	Reference designator	Volatility description	User accessible for external data	Remedial action (action necessary to erase data)
SSD drive(s)	M.2 – 2280/30 NGFF1 NGFF2	Non-volatile magnetic media, various sizes in GB. SSD (solid-state flash drive).	Yes	Low-level format
System BIOS/EC	U2504 (32 MB) RPMC + U2501 (16 MB)- Non-RPMC	Non-volatile memory, 256 Mbits (32 MB), System BIOS and Video BIOS for basic boot operation, ePSA (onboard diags), PXE diags.	No	N/A
USB Type-C PD	U7201	Non-volatile memory, 128 KB for USB Type-C PD F/W.	No	N/A
System memory – DDR5 memory	Two SO-DIMM integrated DDR5 memory: DIMM1/DIMM2	Volatile memory in an OFF state (see State Definitions later in the text). One or two modules are populated. System memory size depends on DIMM modules and is between 8 GB and 64 GB.	Yes	Turn off the computer
System memory SPD EEPROM	On memory DIMM(s)	Non-volatile EEPROM memory. 512 bytes. One Device present on each DIMM. Stores memory manufacturer data and timing information for correct operation of the system memory.	No	N/A
RTC CMOS	BATTERY BT1	Non-volatile memory is 256 bytes. Stores CMOS information.	No	Remove the onboard coin-cell battery
Video memory – frame buffer	For UMA platform: Using system memory	Volatile memory in an OFF state. UMA uses main system memory size that is allocated out of the main memory.	No	Enter S3-S5 state below
Intel ME Firmware	Combine on BIOS ROM	Non-volatile memory, Intel ME firmware for system configuration, security, and protection.	No	N/A
TPM Controller NPCT760JABYX	U9101	Non-volatile memory, 192 K bits (24 K bytes) ROM.	No	N/A
Embedded Flash memory in embedded controller MICROCHIP DEC1515H-D0-I/Z2	EC1	The two SRAM blocks in the DEC1515 are total of 256 KB. The DEC1515 contains a 64 KB block of ROM. EC uses 1 MB with SPI ROM by the G3 sharing mode.	No	N/A

**⚠ CAUTION: All other components on the system board erase data if power is removed from the system. Primary power loss (unplugging the power cord and removing the battery) destroys all user data on the memory (DDR5, 5600 MHz). Secondary power loss (removing the onboard coin-cell battery) destroys system data on the system configuration and time-of-day information.**