

Statement of Volatility - Dell Pro 14 Plus PB14250

△ CAUTION: A CAUTION indicates either potential damage to hardware or erasure of data and tells you how to avoid the problem.

The Dell Pro 14 Plus PB14250 contains both volatile and non-volatile components. Volatile components erase their data immediately after power is removed from the component. Non-volatile components continue to retain their data even after power is removed from the component. The following non-volatile components are present on the Dell Pro 14 Plus PB14250's system board.

Table 1. List of non-volatile components on system board

Description Reference designator		Volatility description	User accessible for external data	Remedial action (action necessary to erase data)
LCD Panel EEDID EEPROM	Part of panel assembly	Non-volatile memory, stores panel manufacturing information, display configuration data	No	Not applicable
System BIOS/EC	UE1 (EC MEC5407) UC2	Non-volatile memory, video BIOS for basic boot operation, PSA (on board diags), PXE diags.	No	Not applicable
System Memory - LPDDR5x	Integrated Memory Controller	Volatile memory in OFF state	No	Not applicable
System memory SPD EEPROM	Not applicable	Non-volatile memory	No	Not applicable
RTC CMOS	UC1	Non-volatile memory, 256 bytes. Stores CMOS information.	No	Not applicable
Video memory – frame buffer	For UMA platform: using system LPDDR5x	Volatile memory in OFF state. UMA uses main system memory size allocated out of main memory.	No	Power off system
Security controller Serial Flash Memory	U1 (upsell USH daughter board)	Non-volatile memory, 128 Mbit (16 Mbyte)	No	Not applicable
SSD drive(s)	M.2 – 2230	Non-volatile magnetic media, various sizes in GB. SSD (solid state flash drive).	Yes	Low level format
TPM Controller	UK101	Non-volatile memory, 328K bits ROM	No	Not applicable
Thunderbolt EEPROM	UT4	Non-volatile memory, 8 Mbit (1 M bytes) flash memory (Thunderbolt FW)	No	Not applicable
Thunderbolt PD EEPROM	Combine on BIOS ROM	Non-volatile memory	No	Not applicable
-		Non-volatile memory	No	Not applicable
Digital PMIC VR controller			No	Not applicable
Intel ME Firmware	Combine on BIOS ROM	Non-volatile memory, Intel ME firmware for system configuration, security and protection	No	Not applicable
Security Controller Serial Flash Memory	Combine on BIOS ROM	Non-volatile memory	No	Not applicable
ISH	Combine on BIOS ROM	Non-volatile component	No	Not applicable
MIPI camera FW	UDV22(up-sell Middle board)	Non-volatile memory, 128 Mbit (16Mbyte)	No	Not applicable

△ CAUTION: All other components on the system board erase data if power is removed from the system. Primary power loss (unplugging the power cord and removing the battery) destroys all user data on the memory (DDR4, 2667 MHz). Secondary power loss (removing the on-board coin-cell battery) destroys system data on the system configuration and time-of-day information.

In addition, to clarify memory volatility and data retention in situations where the system is put in different ACPI power states the following is provided (those ACPI power states are S0, S1, S3, S4 and S5):

- S0 state is the working state where the dynamic RAM is maintained and is read/write by the processor.
- S1 state is a low wake-up latency sleeping state. In this state, no system context is lost (CPU or chip set) and hardware maintains all system contexts.
- S3 is called "suspend to RAM" state or stand-by mode. In this state the dynamic RAM is maintained. Dell systems will be able to go to S3 if the OS and the peripherals used in the system supports S3 state. Win10 support S3 state.
- S4 is called "suspend to disk" state or "hibernate" mode. There is no power. In this state, the dynamic RAM is not maintained. If the system has been commanded to enter S4, the OS will write the system context to a non-volatile storage file and leave appropriate context markers. When the system is coming back to the working state, a restore file from the nonvolatile storage can occur. The restore file has to be valid. Dell systems will be able to go to S4 if the OS and the peripherals support S4 state. Win10 support S4 state.
- S5 is the "soft" off state. There is no power. The OS does not save any context to wake up the system. No data will remain in any component on the system board, i.e. cache or memory. The system will require a complete boot when awakened. Since S5 is the shut off state, coming out of S5 requires power on which clears all registers.

The following table shows all the states supported by Dell Pro 14 Plus PB14250:

Model Number	S0	S1	S3/Modern Standby	S4	S5
Dell Pro 14 Plus PB14250	X	-	X	X	X

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