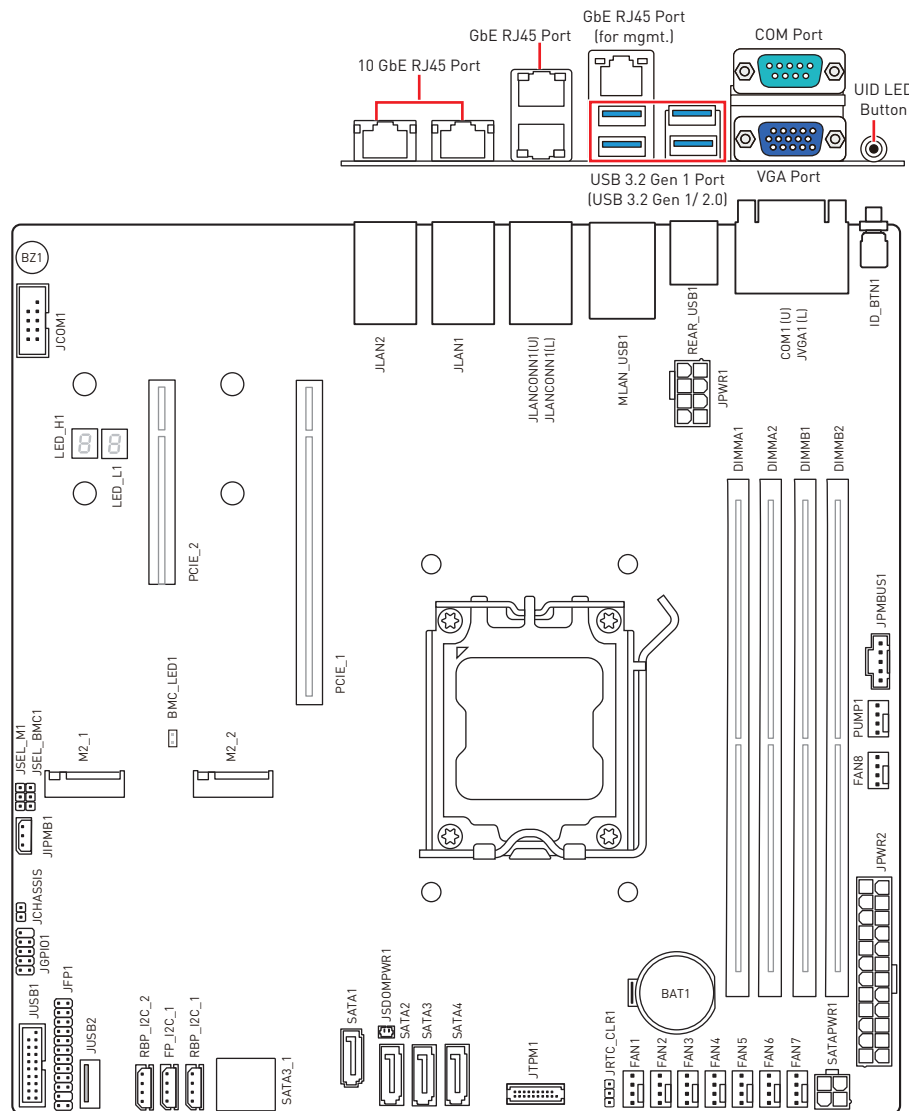


S1102 / D3051

QUICK START GUIDE



CPU

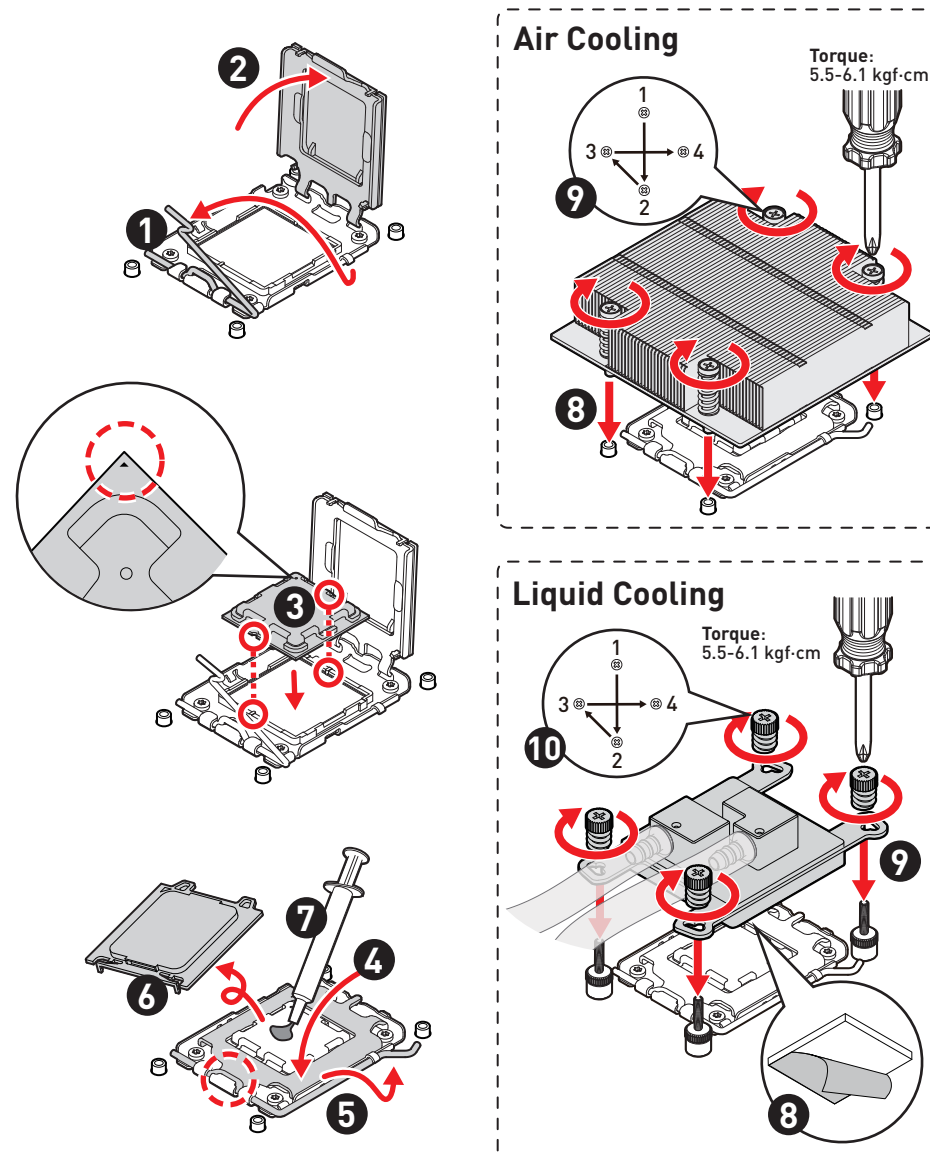
1 x AMD® Ryzen™ 7000 Series Processor

- Up to 16 cores, TDP up to 170W

Front Panel Connector

JFP1	24		23	
	2		1	

CPU and Cooler Installation



Connectors, Jumpers and LED Indicators

Name	Description
JPWR1	8-pin 12V power connector
JPWR2	24-pin main power connector
SATAPWR1	4-pin SATA power connector
JSDOMPWR1	2-pin SATA DOM power connector
SATA1~4	SATA 3.0 6Gb/s ports
SATA3_1	SATA 2.0 3Gb/s ports
M2_1~2	M.2 slots (M Key, PCIe 4.0, 22110/ 2280)
FAN1~8, PUMP1	Fan power connectors
JFP1	Front panel connector
JTPM1	SPI TPM module box header
JGPIO1	GPIO header
JPMBUS1	PMBus box header
JCHASSIS1	Chassis intrusion header
JIPMB1	IPMB box header
FP_I2C_1, RBP_I2C_1~2	I2C box headers
JCOM1	COM port box header
JUSB1	USB 3.2 port box header

Name	Description
JUSB2	USB 2.0 Type-A connector
REAR_USB1, USB1	USB 3.2 Gen 1 Type-A connectors (USB 3.2 Gen 1/ USB 2.0) (on rear side)
PCIE_1	PCIe 5.0 x 16 slot (Gen 5 x 16 signal from CPU)
PCIE_2	PCIe 4.0 x 8 slot (Gen 4 x 4 signal from CPU)
BMC_LED1	BMC Heartbeat LED
LED_H1, LED_L1	Port 80 Debug LEDs
JLAN1~2	10 GbE RJ45 ports (on rear side)
MLAN1	GbE RJ45 port (for mgmt.)(on rear side)
JLANCONN (U, L)	GbE RJ45 ports (on rear side)
COM1	COM port (on rear side)
JVGA1	VGA display connector (on rear side)
JSEL_M1	M2_1 / PCIE_2 selection (default set to pin 1-2, M2_1 enabled)
JSEL_BMC1	BMC selection (default pin 1-2, Using CPU)
JRTC_CLR1	CMOS clear (default pin 1-2 normal)

Memory

The server board offers compatibility with DDR5 ECC/ non-ECC UDIMMs, supporting memory speeds up to 5200MT/s (2DPC).



Recommended Memory Population

Quantity of DIMMs		1	2			3		4
Channel B	DIMMB2		V	V		V	V	V
	DIMMB1			V		V		V
Channel A	DIMMA2	V	V		V	V	V	V
	DIMMA1				V		V	V
"V" indicates a populated DIMM slot.								

"V" indicates a populated DIMM slot.



- Only support **UDIMM**.
- There should be at least 1 DDR5 DIMM populated.
- Paired memory installation for Max performance.
- If only **1 DIMM** is populated in a channel, then populate it in the **DIMMA2** slot.
- Populate the same DIMM type in each channel, specifically: 1. Use the same DIMM size; 2. Use the same number of ranks per DIMM.
- We don't suggest other memory installation.