

## KVR56U46BD8-48

48GB 2Rx8 6G x 64-Bit

PC5-5600 CL46 288-Pin DIMM

### DESCRIPTION

This document describes ValueRAM's KVR56U46BD8-48 is a 6G x 64-bit (48GB) DDR5-5600 CL46 SDRAM (Synchronous DRAM), 2Rx8, memory module, based on sixteen 3G x 8-bit FBGA components. The SPD is programmed to JEDEC standard latency DDR5-5600 timing of 46-45-45 at 1.1V. Each 288-pin DIMM uses gold contact fingers. The electrical and mechanical specifications are as follows:

### FEATURES

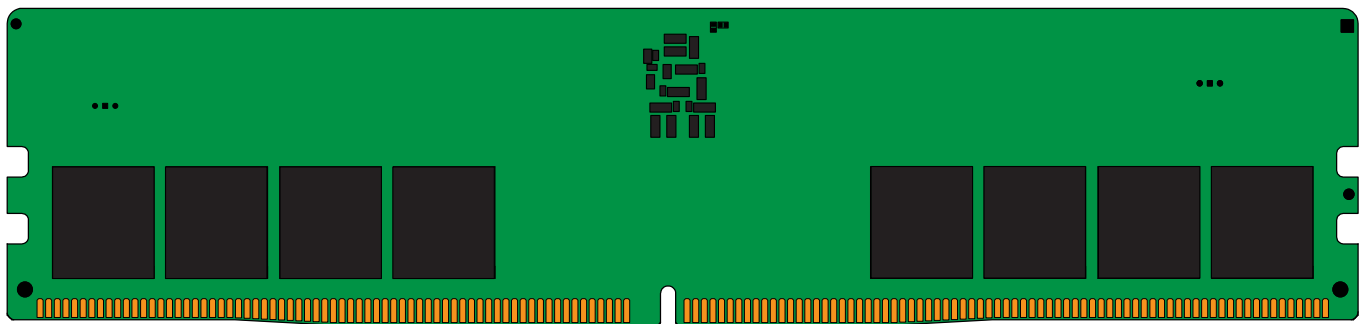
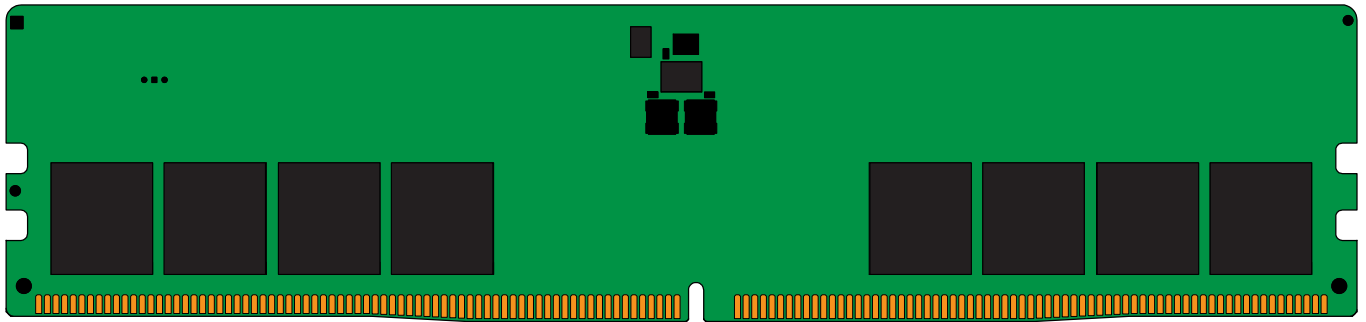
- Power Supply: VDD = 1.1V Typical
- VDDQ = 1.1V Typical
- VPP = 1.8V Typical
- VDDSPD = 1.8V to 2.0V
- On-Die ECC
- PCB: Height 1.23" (31.25mm)
- RoHS Compliant and Halogen-Free

### SPECIFICATIONS

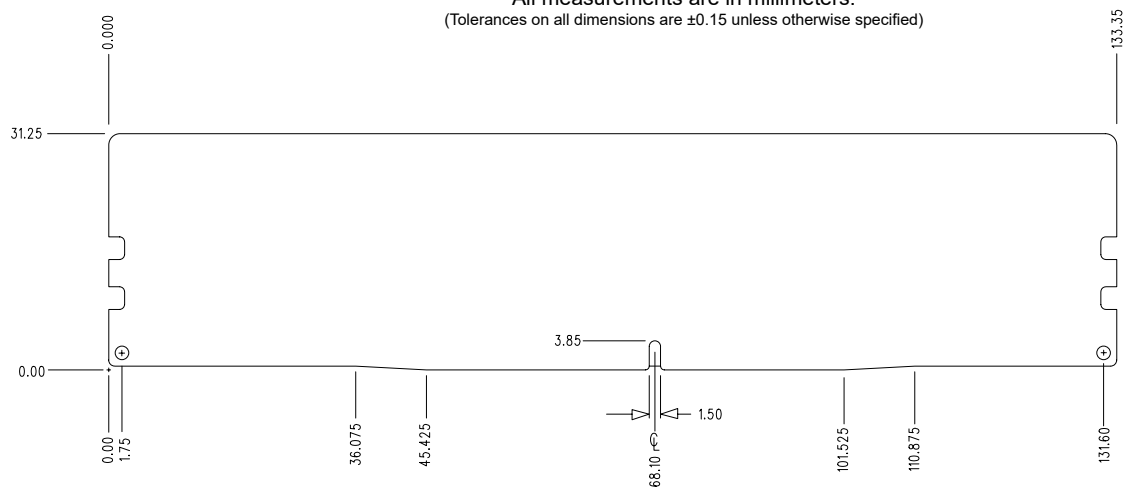
|  |                   |
|--|-------------------|
| CL(IDD)  | 46 cycles         |
| Row Cycle Time (tRCmin)                          | 48ns(min.)        |
| Refresh to Active/Refresh Command Time (tRFCmin) | 295ns(min.)       |
| Row Active Time (tRASmin)                        | 32ns(min.)        |
| UL Rating  | 94 V - 0          |
| Operating Temperature                            | 0° C to +85° C    |
| Storage Temperature                              | -55° C to +100° C |

Continued >>

### MODULE DIMENSIONS



All measurements are in millimeters.  
(Tolerances on all dimensions are  $\pm 0.15$  unless otherwise specified)



The product images shown are for illustration purposes only and may not be an exact representation of the product.  
Kingston reserves the right to change any information at anytime without notice.

| Revision No. | History         | Release Date | Remark | Editor   | Approved |
|--------------|-----------------|--------------|--------|----------|----------|
| A            | Initial Release | 06/22/23     |        | David Y. | Ben L.   |

\*Products and specifications discussed herein are for evaluation and reference purposes only and are subject to change without notice. All information discussed herein is provided on an “as is” basis, without warranties of any kind.