

Broadcom<sup>®</sup> 96xx PCle 4.0, 24G SAS MegaRAID<sup>™</sup> and eHBA Tri-Mode Storage Adapters

User Guide Version 2.5

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## **Overview**

The adapters, based on a 24G SAS tri-mode controller, are high-performance PCIe-to-SATA/SAS/PCIe (tri-mode) storage adapters. Broadcom® tri-mode SerDes technology enables operation of SAS, SATA, or PCIe (NVMe) storage devices in a single drive bay. A single controller can operate in all three modes concurrently: SAS, SATA, and PCIe/NVMe. The adapters negotiate between the speeds and the protocols to recognize and concurrently interface with these three storage devices types.

The adapters provide the following storage interface data transfer rates:

- SAS data transfer rates of 22.5Gb/s, 12Gb/s, 6Gb/s, and 3Gb/s per phy
- SATA transfer rates at 6Gb/s and 3Gb/s per phy
- PCIe (NVMe) data transfer rates of 16 GT/s, 8 GT/s, 5 GT/s, and 2.5 GT/s per lane

The following tables summarize key adapter features.

Table 1: MegaRAID Tri-Mode Storage Adapter and eHBA 9620-16i Features

Adapter	9670W-16i	9670-24i	9660-16i	9620-16i
Ports	16 internal	24 internal	16 internal	16 internal
I/O Processor	SAS4116W	SAS4124	SAS4116	SAS4016
Host Interface	x16 PCle 4.0	x8 PCle 4.0	x8 PCIe 4.0	x8 PCIe 4.0
Storage Interface	SAS, SATA, and PCIe (NVMe)	SAS, SATA, and PCIe (NVMe)	SAS, SATA, and PCIe (NVMe)	SAS, SATA, and PCIe (NVMe)
Form Factor	FH-MD2	FH-MD2	LP-MD2	LP-MD2
RAID Levels	0, 1, 5, and 6	0, 1, 5, and 6	0, 1, 5, and 6	0 and 1
Cache Memory	8 GB, dual channel, 3200 MT/s, DDR4 SDRAM	8 GB, single channel, 3200 MT/s, DDR4 SDRAM	4 GB, single channel, 3200 MT/s, DDR4 SDRAM	_
Storage Interface Connectors	Two SFF-8654 x8	Three SFF-8654 x8	Two SFF-8654 x8	Two SFF-8654 x8
Cache Protection	Yes	Yes	Yes	_
Energy Backup	CVPM05 module	CVPM05 module	CVPM05 module	_

Table 2: eHBA Tri-Mode Storage Adapter Features

Adapter	9600-24i	9600-16i	9600-8i8e	9600W-16e	9600-16e
Ports	24 internal	16 internal	8 internal 8 external	16 external	16 external
I/O Processor	SAS4024	SAS4016	SAS4016	SAS4016W	SAS4016W
Host Interface	x8 PCIe 4.0	x8 PCle 4.0	x8 PCIe 4.0	x16 PCIe 4.0	x8 PCle 4.0
Form Factor	LP-MD2	LP-MD2	LP-MD2	LP-MD2	LP-MD2
Storage Interface	SAS, SATA, and PCle (NVMe)	SAS, SATA, and PCle (NVMe)	SAS, SATA, and PCle (NVMe)	SAS, SATA, and PCIe	SAS, SATA, and PCle

Adapter	9600-24i	9600-16i	9600-8i8e	9600W-16e	9600-16e
Storage Interface Connectors	Three SFF-8654 x8	Two SFF-8654 x8	One SFF-8654 x8 Two SFF-8674 x4	Four SFF-8674 x4	Four SFF-8674 x4

## **Features**

This chapter describes the features that the adapters support.

#### **RAID** and eHBA Features

The following sections list primary RAID and eHBA features that the adapters support. For a full description of the RAID features, refer to the *MegaRAID Tri-Mode Software User Guide*, which can be found in the Support Documents and Downloads section of the Broadcom website.

## MegaRAID 9670W-16i, 9670-24i, and 9660-16i Adapter RAID Features

The MegaRAID adapters support the following RAID features:

- RAID levels 0, 1, 5, and 6
- RAID spans 10, 50, and 60
- SAS/SATA drives: 240
- NVMe SSDs: 32
- JBOD physical drive (PD) state for SDS environments
- Online Capacity Expansion (OCE)
- Auto resume after loss of system power during array rebuild or OCE
- Single controller multipathing
- Load balancing
- · Fast initialization for quick array setup
- · Check consistency for background data integrity
- SSD support with SSD Guard<sup>™</sup> technology
- · Patrol read for media scanning and repairing
- · Sixty-four virtual drive support
- Disk data format (DDF)-compliant Configuration on Disk (COD)
- Self-Monitoring, Analysis, and Reporting Technology (SMART) support
- · Global and dedicated hot spare with revertible hot spare support:
  - Automatic rebuild
  - Enclosure affinity
  - Emergency SATA hot spare for SAS arrays
- · Enclosure management support:
  - Universal Backplane Management (UBM)
  - SES (inband)
  - SGPIO (sideband)
  - VPP
- DataBolt bandwidth optimizer technology support for compatible expander-based enclosures
- Shield state drive diagnostic technology
- MegaRAID SafeStore<sup>™</sup> software for SED key management

## MegaRAID 9620-16i eHBA Features

The MegaRAID 9620-16i adapter supports the following features:

- RAID levels 0 and 1
- RAID span 10
- SAS/SATA drives: 32
- NVMe SSDs: 32
- · JBOD PD state for SDS environments
- Single controller multipathing
- Load balancing
- Fast initialization for quick array setup
- Check consistency for background data integrity
- SSD support with SSD Guard technology
- · Patrol read for media scanning and repairing
- Four virtual drive support
- DDF-compliant COD
- SMART support
- Global and dedicated hot spare with revertible hot spare support:
  - Automatic rebuild
  - Emergency SATA hot spare for SAS arrays
- Enclosure management support:
  - Universal Backplane Management (UBM)
  - SES (inband)
  - SGPIO (sideband)
  - VPP
- DataBolt bandwidth optimizer technology support for compatible expander-based enclosures
- Shield state drive diagnostic technology
- · MegaRAID SafeStore software for SED key management

## eHBA 9600 Adapter Features

The eHBA 9600 adapters support the following eHBA features:

- SAS/SATA devices: 240
- NVMe SSDs: 32
- · Shingled magnetic recording (SMR) drive support
- Multi-actuator drive support

## **Operating System Support**

The tri-mode storage adapters support the operating systems in the following list.

For specific version information, refer to the *MegaRAID Tri-Mode Device Driver Installation User Guide*, which you can download from http://www.broadcom.com/support/download-search.

- Microsoft Windows
- VMware<sup>®</sup> vSphere/ESXi
- · Red Hat Enterprise Linux
- SuSE Linux
- Ubuntu Linux
- Citrix XenServer
- CentOS Linux
- Debian Linux
- Oracle Enterprise Linux
- Fedora
- FreeBSD

Firmware and drivers are routinely updated. Visit the Broadcom Support Documents and Downloads page to download the latest firmware and driver for the adapter.

## **PCIe Host Interface**

The adapter's PCIe 4.0 host interface provides maximum transmission and reception rates of up to 128 GT/s (16 GB/s per lane). The tri-mode controller uses a packet-based communication protocol to communicate over the serial interconnect. Other PCIe host interface features include the following:

- · Eight-lane or sixteen-lane PCIe host interface
- PCle hot-plug
- · Power management:
  - Supports the PCI Bus Power Management Interface Specification Revision 1.2
  - Supports Active State Power Management, including the L0 states, by placing links in a power-saving mode during times of no link activity
- Error handling
- · High bandwidth per pin with low overhead and low latency
- · Lane reversal and polarity inversion
- Single-phy (one-lane) link transfer rate of 16 GT/s, 8 GT/s, 5 GT/s, and 2.5 GT/s in each direction
- Eight-lane aggregate bandwidth of up to 16 GB/s (16,000 MB/s)
- Sixteen-lane aggregate bandwidth of up to 32 GB/s (32,000 MB/s)
- Support for x16, x8, x4, x2, and x1 link widths

## **LED Management**

Broadcom adapter LED management support differs between internal and external connecting adapters.

The internal adapters offer LED management support for SAS/SATA backplanes and (PCIe) NVMe backplanes. External connect adapters offer enclosure LED management support for enclosure implementations through SES. See Backplane Management for more information.

## **Tri-Mode Storage Interface Features**

The adapter's storage interface supports concurrent operation with SAS, SATA, and PCIe (NVMe) devices to provide a fully functional solution for any storage environment.

· PCIe (NVMe) interface features:

- Up to sixteen x1, eight x2, or four x4 NVMe direct-attach drive support
- Data transfer at 16 GT/s, 8 GT/s, 5 GT/s, and 2.5 GT/s
- Independent resets and configuration
- Common reference clock and separate reference clock independent SSC (SRIS) support
- SAS features:
  - SAS data transfers at 22.5Gb/s, 12Gb/s, and 6Gb/s
  - DataBolt technology on all SAS phys to improve performance
  - Serial, point-to-point, enterprise-level storage interface
  - Wide ports that contain multiple phys
  - Narrow ports that contain a single phy
  - SAS phy power management
  - Data transfer by using SCSI information units
  - T10 data protection management
  - Support for persistent connection capability
  - Support for SPL-3 initiate close capability
  - Configurable Rx and Tx polarity inversion
  - Configurable phy-to-disk mapping
  - Configurable SSC
- SATA interface features:
  - SATA and STP data transfers at 6Gb/s
  - Addressing of multiple SATA targets through an expander

# **Tri-Mode Storage Interface**

The adapter uses one or two CSW blocks for the storage interface.

The tri-mode interface groups phys into the CSW blocks that contain 16 phys each. Limitations exist on how the phys can be grouped to create wide ports for SAS/SATA or multilane ports for PCIe.

The following table indicates how the connectors map to the phys within each CSW. Card layout figures in Broadcom MegaRAID and eHBA Tri-Mode Storage Adapter Characteristics show the connector designations for each adapter.

**Table 3: Adapter Connector-to-CSW Port Associations** 

Adapter	Connector 0	Connector 1	Connector 2	Connector 3
9670W-16i	CSW1[0:7]	CSW1[8:15]	_	_
9670-24i	CSW1[0:7]	CSW1[8:15]	CSW0[8:15]	_
9660-16i	CSW0[0:7]	CSW1[0:7]	_	_
9620-16i	CSW1[8:15]	CSW0[8:15]	_	_
9600-24i	CSW1[8:15]	CSW0[8:15]	CSW1[0:7]	_
9600-16i	CSW1[8:15]	CSW0[8:15]	_	_
9600-8i8e	CSW1[0:7]	CSW0[12:15]	CSW0[8:11]	_
9600W-16e	CSW0[12:15]	CSW0[8:11]	CSW0[4:7]	CSW0[0:3]
9600-16e	CSW0[12:15]	CSW0[8:11]	CSW0[4:7]	CSW0[0:3]

The internal adapters can direct attach to SAS, SATA, or NVMe drives. The internal and external adapters support drive attach through PCle switches or expanders.

#### **NOTE**

Carefully assess any decision to mix SAS and SATA drives within the same virtual drive (VD). Although you can mix drives, the practice is discouraged.

MegaRAID does not permit mixing SAS and NVMe drives or SATA and NVMe drives within the same VD. To mix NVMe and SAS/SATA drives on a MegaRAID adapter, you must configure the drives in separate VDs.

The following sections describe the connector options for a single direct-attach type solution. Adhere to the same guidelines if you combine device types.

## **SAS/SATA Support**

The storage interface is comprised of either 24 phys or 16 phys, depending on the adapter. Dedicated SAS phy management hardware manages the phys in groups of eight within each CSW: CSW0[0:7], CSW0[8:15], CSW1[0:7], and CSW1[8:15]. Depending on the adapter, one or more of these CSW groups are used for the SAS/SATA interfaces and these SAS phy management hardware instances cannot communicate with each other.

When you configure a wide port, the connections must attach exclusively to phys all managed by the same CSW group. If the ports are not managed by the same CSW group, unexpected controller and host behavior occurs. You can create combinations of a x1 to x8 wide link within CSW0[0:7], CSW0[8:15], CSW1[0:7], or CSW1[8:15]. You cannot create a wide link by spanning RX/TX pairs between CSW0 and CSW1 or between phys 8:15 and 0:7 within the same CSW group.

## PCIe (NVMe) Support

The following table shows how many NVMe drives or Broadcom PEX88000-series switches can directly attach to each adapter.

The 9600W-16e adapter, 9600-16e adapter, and 9600-8i8e adapter (external ports) do not support direct attach to NVMe drives. The expected topology for these adapters is a typical just a bunch of flash (JBOF) scenario that uses a switch to connect the NVMe drives.

#### **NOTE**

Connected NVMe drives must support end-to-end CRC (ECRC).

Table 4: NVMe Device or PCle Switch Direct-Attach Options Supported for Each Adapter

Adapter	x4 NVMe Drives	x2 NVMe Drives	x1 NVMe Drives	x16 Switch	x8 Switches	x4 Switches
9670W-16i	4	8	16	1	2	4
9670-24i	6	12	24	1	3	6
9660-16i	4	8	16	0	2	4
9620-16i	4	8	16	0	2	2
9600-24i	6	12	24	0	3	6
9600-16i	4	8	16	0	2	4
9600-8i8e <sup>a</sup>	2	4	8	0	2	4
9600W-16e	0	0	0	1	2	4
9600-16e	0	0	0	1	2	4

The adapter phys are grouped into two CSWs: CSW1[0:15] and CSW0[0:15]. Depending on the adapter, 8 or 16 of these phys are used consecutively for the PCIe host interface and the remaining tri-mode phys are available for connection to any supported SAS, SATA, or PCIe (NVMe) storage devices. The following tables indicate supported topologies. Typical backplane designs naturally align with these topology rules, but you must take care not to design anything atypical that might interfere with the adapter's operation.

Table 5: 9670W-16i Adapter PCle Topology Configuration Combinations

			Conne	ector 0				Connector 1										
			CSW	1[0:7]				CSW1[8:15]										
0	1	2	3	4	5	6	7	8 9 10 11 12 13 14										
							X.	16										
	_		Х	8				x8										
	_		Х	8					Х	4			Х	4				
	Х	4			Х	4			Х	4			Х	4				
	_		Х	8				х	2	х	2	х	2	х	2			
	Х	4			х	4		х	x2 x2 x2						2			
Х	:2	Х	2	х	2	х	2	х	x2 x2 x2									

a. Only internal internal ports support direct-attach NVMe drives.

			Conne	ector 0				Connector 1									
			CSW	1[0:7]							CSW1	[8:15]					
0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15		
	x8 x1 x1 x1 x1 x1 x1 x1													x1			
	Х	4			Х	4		x1	x1	x1	x1	x1	x1	x1	x1		
x1	x1	x1	x1	x1	x1	x1	x1	x1	x1	x1	x1	x1	x1	x1	x1		
	x4 x4										Х	8					
Х	(2	х	2	х	2	х	2				х	(8					
x1	x1	x1	x1	x1	x1	x1	x1				х	(8					
Х	(2	х	2	х	2	х	2	x4 x4									
x1	x1	x1	x1	x1	x1	x1	x1	x4 x4									

Table 6: 9660-16i Adapter PCle Topology Configuration Combinations

			Conne	ector 0							Conn	ector 1	'								
			csw	0[0:7]							CSW	/1[0:7]									
0	1	2	3	4	5	6	7	0	1	2	3	4	5	6	7						
		•	×	8				x8													
			х	8					х	:4			X <sup>2</sup>	1							
	х	4			x	4			x	:4			X <sup>2</sup>	1							
			х	8				х	2	х	2	x	2	х	2						
	Х	:4			x	4		х	2	х	2	x	2	х	2						
×	(2	х	2	х	2	<b>)</b>	(2	х	2	х	2	x	2	х	2						
			х	8				x1 x1 x1 x1 x1 x1 x1 x1													
	х	:4			x	4		x1	x1	x1	x1	x1	x1	x1	x1						
x1	x1	x1	x1	x1	x1	x1	x1	x1	x1	x1	x1	x1	x1	x1	x1						
	х	х4			<del></del>			x4 x4										x8			
×	(2	х	2	х	2	<b>)</b>	(2					x8									
x1	x1	x1	x1	x1	x1	x1	x1					x8									
×	(2	х	x2 x2			x2 x2				4			X <sup>2</sup>	1							
x1	x1	x1	x1	x1	x1	x1	x1		х	:4			X <sup>2</sup>	1							

Table 7: 9670-24i Adapter PCle Topology Configuration Combinations

		C	Conne	ctor	0			Connector 1								Connector 2								
			CSW <sup>2</sup>	1[0:7]				CSW1[8:15]								CSW0[8:15]								
8 9 10 11 12 13 14 15														00	11	22	33	44	55	66	77			
	x16	(paire	ed wit	h Cor	necto	r 2)		x8							x16 (paired with Connector 0)									
	x16 (paired with Connector 2) x4 x4														x16	(pair	ed wit	h Con	necto	r 0)				
x16 (paired with Connector 2) x2 x2 x2 x2												2		x16	(pair	ed wit	h Con	necto	r 0)					
	x16	(paire	ed wit	h Cor	necto	r 2)		x1	x1	x1	x1	x1	x1	x1	x1	x16 (paired with Connector 0)								
					•		the pr				•						-		Х	8				
					•		the pr				•						×	4			х	4		
	PCIe Topology Configuration Combinations table.  Use any row from the previous 9660-16i Adapter PCIe Topology Configuration Combinations table.														×	2	×	2	х	2	х	2		
	Use any row from the previous 9660-16i Adapter PCle Topology Configuration Combinations table.													x1	x1	x1	x1	x1	x1	x1	x1			

Table 8: 9600-16i Adapter and 9620-16i Adapter PCle Topology Configuration Combinations

			Conne	ector 0							Conne	ector 1					
			CSW1	[8:15]							cswo	[8:15]					
								88	99	1010	1111	1212	1313	1414	1515		
			Х	8				x8									
	_		х	8				x4 x4									
	Х	4			х	4			Х	Х	4						
			х	8				х	2	х	2	х	2	х	2		
	Х	4			Х	4		х	2	х	2	х	2	х	2		
Х	2	х	2	Х	2	х	2	х	2	х	2	х	2	х	2		
			Х	8				x1	x1 x1 x1 x1 x1 x1 x1								
	Х	4			Х	4		x1	x1	x1	x1	x1	x1	x1	x1		
x1	x1	x1	x1	x1	x1	x1	x1	x1	x1	x1	x1	x1	x1	x1	x1		
	Х	4			Х	4					Х	8					
Х	2	Х	2	Х	2	х	2				Х	8					
x1	x1	x1	x1	x1	x1	x1	x1				Х	8					
Х	2	х	2	x2			2		×	:4			Х	4			
x1 x1 x1 x1 x1 x1 x1 x								x4 x4									

Table 9: 9600-24i Adapter PCle Topology Configuration Combinations

Connector 0							Connector 1					Connector 2											
		(	CSW1	[8:15	]			CSW0[8:15]						CSW1[0:7]									
8	9	10	11	12	13	14	15	8	9	10	11	12	13	14	15	0	1	2	3	4	5	6	7
			Х	8							Х	8							>	(8			
			Х	8					Х	4			Х	4				x4			Х	4	
	X	4			Х	4		х	2	х	2	х	2	х	2	,	(2		x2	Х	(2	х	2
			Х	8				Х	2	х	2	х	2	х	2	x1	x1	x1	x1	x1	x1	x1	x1
	Х	4			Х	4		х	2	х	2	х	2	Х	2				ny of t				
,	(2	х	2	х	2	х	2	x1	x1	x1	x1	x1	x1	x1	x1				ny of t				
		•	х	8		•		x1	x1	x1	x1	x1	x1	x1	x1	Combine with any of the first four configurations in this column.							
	×	:4			х	:4		x1	x1	x1	x1	x1	x1	x1	x1	Combine with any of the first four configurations in this column.							
x1	x1	x1	x1	x1	x1	x1	x1			•	×	8			•				ny of t				
	х	4			х	4					х	8				Combine with any of the first four configurations in this column.							
>	(2	х	2	х	2	х	2				х	8							ny of t				
x1	x1	x1	x1	x1	x1	x1	x1		×	4			х	4					ny of t				
>	x2 x2 x2 x2			x4 x4				Combine with any of the first four configurations in this column.															
x1	x1	x1	x1	x1	x1	x1	x1		Х	:4			х	4					ny of t				

Table 10: 9600W-16e Adapter and 9600-16e Adapter PCle Topology Configuration Combinations

	Connector 0 Connector 1				Connector 2				Connector 3						
	CSW0[12:15] CSW0[8:11]			CSW0[4:7]				CSW0[0:3]							
12	13	14	15	8	9	10	11	4 5 6 7			0	1	2	3	
							Χ´	16							
	x8							x8							
x4 x4							X	4			x4				

**Table 11: 9600-8i8e Adapter PCle Topology Configuration Combinations** 

Connector 0							Connector 1				Connector 2				
	CSW1[0:7]					CSW0[12:15]				CSW0[8:11]					
0	1	2	3	4	5	6	7	12	13	14	15	8	9	10	11
x8						x8									
	х	4			х	4			x4 x4						
Х	2	х	2	х	2	х	2	_							
x1	x1	x1	x1	x1	x1	x1	x1	_							

# **Common REFCLK Support**

Connections that require a common REFCLK include one REFCLK for each quadrant. Each connector is divided into two quadrants.

The adapter uses x8 SFF-8654 (SlimSAS) connectors. For x4 NVMe or PCle switch connections, the REFCLK sourced by each guadrant directly clocks each attached x4 PCle connection.

To directly attach x2 or x1 NVMe drives that require a common REFCLK, where more than one drive is sourced from a single quadrant, you must properly fan out the shared REFCLK on the backplane. For x2 and x1 NVMe connections, use SRIS-enabled drives to avoid fanning the clock out on the backplane.

# **Backplane Management**

Use the information in this section to setup the adapter's backplane management options.

The SFF-8448 standard defines how to detect whether the backplane supports a SGPIO or two-wire interface (I<sup>2</sup>C) for SAS/SATA usage. SFF-9402 is a superset of SFF-8448, adding the PCIe-defined sideband signal, which means that SAS/SATA users see no change in backplane management detection when using the adapters.

## **Universal Backplane Management**

The adapters provide LED operation and other backplane management of NVMe only, SAS/SATA only, or mixed-protocol backplanes based on the SFF-TA-1005 specification.

SFF-TA-1005 is an industry-standard backplane management specification commonly known as UBM. As long as the backplane management controller is designed in accordance with the UBM specification, the adapter automatically detects the backplane type and functions appropriately.

The adapter supports the industry-standard *SFF-TA-1005 Specification for Universal Backplane Management (UBM)*. UBM provides the following key features:

- Reports the backplane capabilities, including the following:
  - NVMe drive widths
  - Common REFCLK or separate REFCLK support
  - Maximum speeds
  - Designed slot power
- Supports cable order independence, that is, the drive LED control and slot ID are not dependent on cable order.
- Enables drive hot plug insertion through control of PERST# timing.

For existing SAS/SATA backplanes, if BP\_TYPE = 0, the adapter uses SGPIO for legacy backplane management. Refer to the SFF-8485 specification for functionality details. Design new backplanes with the industry-standard SFF-TA-1005 (UBM) specification for backplane management.

## **Virtual Pin Port Management**

Broadcom requires new designs to enable UBM for backplane management.

The adapter maintains support for Virtual Pin Port (VPP) backplane management for legacy implementations. The adapters provide LED operation for NVMe devices based on the VPP over I<sup>2</sup>C definition. Standard VPP implementation calls for one PCA9555 target per two devices. For each drive pair, the adapter expects to see one PCA9555 target responding to address 0x4C on each pair of NVMe drives.

# Sideband Signals

The internal adapters have one or two x8 SFF-8654 connectors. Each x8 connector provides two sets of sidebands. This section describes the sideband signal usage. The following table defines the sideband signal's pins on the SFF-8654 connector. The last column in the table indicates the strength of the pull-up resistor or pull-down resistor values on the adapter. See Table 13, Sideband Management Pin Settings, for the signal descriptions, and see Table 14, Internal x8 SFF-8654 Connector Pinout, for a complete connector pinout.

**Table 12: Sideband Signal Pinout** 

Connector A Side	Connector B Side	Sideband or Vendor Specific Pin Number	UBM Assignments	Direction	Resistor Value
A8	A26	7	BP_TYPE	Input	100-kΩ pull-down
A9	A27	4	2W_RESET#	Output	2.0-kΩ pull-up
A10	A28	3	GND	_	<u> </u>
A11	A29	+	REFCLK+	Output	_
A12	A30	-	REFCLK-	Output	<u> </u>
B8	B26	0	2W_CLK	Input/Output	2.0-kΩ pull-up
B9	B27	1	2W_DATA	Input/Output	2.0-kΩ pull-up
B10	B28	2	GND	_	_
B11	B29	5	PERST#	Input/Output	2.0-kΩ pull-up
B12	B30	6	C_TYPE, D_INPL#, CHANGE_DET#	Input/Output	10-kΩ pull-up

The following table describes the sideband signal pin settings.

**Table 13: Sideband Management Pin Settings** 

Pin Name	Settings	Description
BP_TYPE	0: SGPIO     1: Two-wire interface	Indicates if the backplane uses SGPIO or two-wire interface for management.  To maintain backwards compatibility with SPGIO-based backplanes, the adapter has a weak pull-down to default to SGPIO if the backplane does not explicitly drive the signal.
2W_RESET#	<ul><li>0: Reset is asserted</li><li>1: Reset is not asserted</li></ul>	Optional reset driven by the host if the UBM target reports that the target can be reset.
REFCLK+/-		PCIe REFCLK HCSL 100-MHz clock driven by the device side ports to PCIe devices that require REFCLK.  If D_INPL# is 0 and BP_TYPE is 1, the adapter enables the REFCLK outputs for that quad of high-speed lanes.  When BP_TYPE is 0 or the UBM Clock Routing bit on the backplane is 0, this output is turned off.
2W_CLK	_	The two-wire interface clock signal.
2W_DATA	_	The two-wire interface data signal.
PERST#	<ul><li>0: Reset is asserted</li><li>1: Reset is not asserted</li></ul>	The adapter drives the PCIe RESET# signal.  This signal uses a clamp to ground so that the signal on the adapter powers up LOW until backplane detection warrants the release of this signal for open-drain use. This method ensures that PERST# does not deassert until the directly connected NVMe drive is successfully detected.

Pin Name	Settings	Description
C_TYPE, D_INPL#, CHANGE_DET#		<ul> <li>Open collector/drain input or output signal.</li> <li>C_TYPE. If BP_TYPE is 0, the adapter drives this signal LOW. If BP_TYPE is 1, this signal adheres to the SFF-8448 requirement to drive this signal to 1 in response to floating the signal. Because this signal is an open drain signal, driving to 1 is when a pull-up resistor pulls this signal HIGH.</li> <li>D_INPL#. When C_TYPE is HIGH, the backplane pulls this signal to ground to indicate an NVMe device is connected and a two-wire interface backplane management target might be on the sideband's two-wire interface.</li> <li>CHANGE_DET#. If D_INPL# is 0 and a UBM FRU device is discovered on the two-wire interface, the UBM FRU data can inform the adapter that the device is CHANGE_DET# feature capable. The adapter can rely on this signal as the CHANGE_DET# signal as described in the UBM specification. In this mode, the UBM controller drives this signal LOW to assert CHANGE_DET#.</li> </ul>

# **Cables and Cabling Configurations**

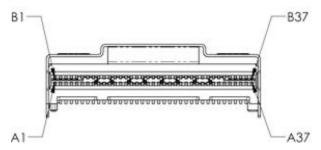
Use the following sections to select or design internal or external connectors and cables.

## **Internal Adapter Connector Pinout**

The internal adapter follows the SFF-9402 standard for connector signal assignments.

Each x8 connector includes eight PCIe transmit and receive lanes and two sets of sidebands designated as A and B, in accordance with the SFF-9402 specification. The following figure shows the x8 SFF-8654 pin designations.

Figure 1: x8 SFF-8654 Pin Designations



The following table defines the adapter's internal SFF-8654 connector pinouts.

Table 14: Internal x8 SFF-8654 Connector Pinout

Pin	Name	Pin	Name
A1	GND	B1	GND
A2	PERp0, RX0+	B2	PETp0, TX0+
A3	PERn0, RX0-	B3	PETn0, TX0-
A4	GND	B4	GND
A5	PERp1, RX1+	B5	PETp1, TX1+
A6	PERn1, RX1-	B6	PETn1, TX1-
A7	GND	B7	GND
A8	BP_TYPEA	B8	2W-CLKA, SClockA
A9	2W_RESETA, SDataOutA	B9	2W-DATAA, SloadA
A10	GND	B10	GND
A11	REFCLKA+	B11	PERSTA#, SDatainA
A12	REFCLKA-	B12	CPRSNTA#, CNTRLR_TYPEA
A13	GND	B13	GND
A14	PERp2, RX2+	B14	PETp2, TX2+
A15	PERn2, RX2-	B15	PETn2, TX2-
A16	GND	B16	GND
A17	PERp3, RX3+	B17	PETp3, TX3+

Pin	Name	Pin	Name
A18	PERn3, RX3-	B18	PETn3, TX3-
A19	GND	B19	GND
A20	PERp0, RX4+	B20	PETp0, TX4+
A21	PERn0, RX4-	B21	PETn0, TX4-
A22	GND	B22	GND
A23	PERp1, RX5+	B23	PETp1, TX5+
A24	PERn1, RX5-	B24	PETn1, TX5-
A25	GND	B25	GND
A26	BP_TYPEB	B26	2W-CLKB, SClockB
A27	2W_RESETB, SDataOutB	B27	2W-DATAB, SLoadB
A28	GND	B28	GND
A29	REFCLKB+	B29	PERSTB#, SDataInB
A30	REFCLKB-	B30	CPRSNTB#, CNTRLR_TYPEB
A31	GND	B31	GND
A32	PERp2, RX6+	B32	PETp2, TX6+
A33	PERn2, RX6-	B33	PETn2, TX6-
A34	GND	B34	GND
A35	PERp3, RX7+	B35	PETp3, TX7+
A36	PERn3, RX7-	B36	PETn3, TX7-
A37	GND	B37	GND

## **Storage Interface Cabling**

Choose the proper cable for the given backplane type and connectors.

The correct choice is especially important for backplanes that use SFF-8643 for the NVMe connectors. Many of these backplanes use an older legacy-recommended pinout for the NVMe connector instead of a connector pinout based on the SFF-9402 specification. Most backplanes that use either SFF-8612 or SFF-8654 connectors follow the SFF-9402 specification. The pinout recommended in the *PCI Express OCuLink Specification* is equivalent to that recommended for SFF-9402. Verify the connector pinout for the intended backplane to make sure the proper cable is used when connecting to NVMe drives.

Broadcom provides the following cables to use for the adapter. Use the MPN listed to order a cable from Broadcom. If you source your own cables, use the Broadcom part number from the following table and the drawings and pinouts in Cable Drawings and Pinouts.

**Table 15: Internal Adapter Cables** 

MPN	Broadcom Cable Part No.	Cable Description	Backplane Connector
05-60001-00	5067-6865	x8 8654 to 2x4 8612, AltWiring 1M	Two x4 SFF-8612 (OCuLink)
05-60002-00	5067-6862	x8 8654 to 2x4 8643 (W), SMC 1M	Two x4 SFF-8643 (mini-SAS HD)
05-60003-00	5067-6866	x8 8654 to 2x4 8643, 9402 SAS 1M	Two x4 SFF-8643 (mini-SAS HD)

MPN	Broadcom Cable Part No.	Cable Description	Backplane Connector
05-60004-00	5067-6103	x8 8654 to 2x4 8654, 9402 1M	Two x4 SFF-8654 (SlimSAS)
05-60005-00	5067-6682	x8 8654 to 2xU.2 Direct, 1M	Two U.2 SFF-8639
05-60006-00	5067-7542	x8 8654 to 8xU.3 Direct, 1M	Eight U.3 SFF-8639
05-60007-00	5067-6869	x8 8654 to 1x8 8654, 9402 1M	One x8 SFF-8654 (SlimSAS)

**Table 16: Adapter Broadcom Cable Use Cases** 

MPN	Туре	Description
05-60001-00	SAS/SATA and NVMe	Use for backplanes with x4 SFF-8612 connectors with pinouts that follow the SFF-9402 specification.
05-60002-00	NVMe	Specialty cable that provides NVMe connections for SuperMicro Purley backplanes. This cable has white SFF-8643 connectors to indicate that it must connect to the white SFF-8643 connectors on the SuperMicro Purley backplanes.
05-60003-00	SAS/SATA	Use for traditional SAS/SATA connections. Usually backplanes designed to support SAS/SATA only or are double plumbed for U.2 and SAS/SATA drives use SFF-8643 connectors.
05-60004-00	SAS/SATA and NVMe	Use for backplanes with x4 SFF-8654 connectors with pinouts that follow the SFF-9402 specification.
05-60005-00	NVMe	Enables direct connect from the adapter to a U.2 NVMe drive. Use for proof-of-concept type applications.
05-60006-00	SAS/SATA and NVMe	Enables direct connect from the adapter to a U.3 NVMe or SAS/SATA drive. This cable does not send a PCle REFCLK or PERST# to each drive connector; that is, the U.3 drive must support SRIS and not require PERST#. Use for proof-of-concept type applications.
05-60007-00	SAS/SATA and NVMe	Use for backplanes with x8 SFF-8654 connectors with pinouts that follow the SFF-9402 specification.

## **Backplane Connectors**

The SFF-8612 or SFF-8654 connectors are the preferred connectors to use for the NVMe backplane or multiprotocol backplanes, based on the SFF-TA-1001 universal bay definition.

Backplanes that use OCuLink connectors should follow the *PCI Express OCuLink Specification*. This pinout is also equivalent to the SFF-9402 specification recommendations. Verify the backplane connector pinout to make sure you use proper cabling to the NVMe drive. Refer to the *PCI Express OCuLink Specification* and the SFF-9402 specification for backplane NVMe connector pinout information.

## **External Adapter Connector Pinout**

External adapters support SAS and PCIe connections.

The PCIe cable specification swaps lanes 0 and 1 compared to the SAS specification. For PCIe connections, this swap means the external pinout must place lanes 0 and 1 on the same pins as the JBOF. This swap does not impact SAS connections because lane ordering does not impact SAS designs.

For external PCIe JBOF connections, as defined by the *PCI Express External Cabling Specification*, the adapter does not connect REFCLK and PERST#. The adapter only supports an SRIS-capable endpoint, that is, no REFCLK. The JBOF handles the drive (connected to the JBOF switch) start-of-day reset, hot insertion, and clocking requirements.

The adapter expects a local (single master) two-wire bus connection to the cable or active module's EEPROM. A cable requires a local EEPROM on each end to identify cable properties, such as length, loss budget, ganging, and so on. The adapter supports no direct communication to the enclosure over two-wire. SES performs enclosure management.

When using active cables with the 9600-16e adapter, eHBA 9600-8e adapter, and eHBA9600-8i8e adapter, the adapter toggles the active cable's Vact port (active cable power), but not the Vman port (management interface power) when doing an online firmware update or for other conditions that result in a soft reset. If your active cable requires Vman to be toggled, the cable must be removed and reinserted, or power to the card slot must be cycled. If neither action is done, the storage link will not be restored. If the active cable requires that only Vact toggle, no power cycle or cable re-insertion is needed.

When using active cables with the 9600-16e adapter, eHBA 9600-8e adapter, or eHBA 9600-8i8e adapter, you must implement an additional step if your active cable requires that the Vman port (management interface power) be toggled. The Broadcom adapter toggles the active cable's Vact port (active cable power), but not the Vman port after an online firmware update or for other conditions that result in a soft reset. If your active cable requires that the Vman port be toggled, the cable must be removed and reinserted, or power to the card slot must be cycled. If neither action is done, the PCIe/SAS link will not be restored. If the active cable requires that only the Vact port toggle, no power cycle or cable reinsertion is needed.

The following table shows the cable pinout for the cable that Broadcom provides for the external adapter. See Cable 05-60009-00 for the cable drawing and pinout. Use the drawing if you source your own cable.

**Table 17: External Adapter Cable Pinout** 

Pin	Signal	Pin	Signal
A1	No Connect	C1	CMICLK
A2	CINT#	C2	CMIDAT
A3	GND	C3	GND
A4	PERp0, RX0+	C4	PETp0, TX0+
A5	PERn0, RX0-	C5	PETn0, TX0-
A6	GND	C6	GND
A7	PERp3, RX3+	C7	PETp3, TX3+
A8	PERn3, RX3-	C8	PETn3, TX3-
A9	GND	C9	GND
B1	PWR	D1	PWR
B2	CBLPRSNT#	D2	MGTPWR
B3	GND	D3	GND
B4	PERp1, RX1+	D4	PETp1, TX1+
B5	PERn1, RX1-	D5	PETn1, TX1-
B6	GND	D6	GND
B7	PERp2, RX2+	D7	PETp2, TX2+
B8	PERn2, RX2-	D8	PETn2, TX2-
B9	GND	D9	GND

Broadcom provides the following cable to use for external adapters. Use the MPN listed to order the cable from Broadcom. If you source your own cables, use the Broadcom part number from the following table, and the drawing and pinout in Cable 05-60009-00.

### **Table 18: External Adapter Cable**

MPN	Broadcom Cable Part No.	Cable Description	Backplane Connector
05-60009-00	5067-9643	G4/S4 x4 8644 to x4 8644, 3M	Two x4 SFF-8674

## CacheVault Data Protection

The MegaRAID Tri-Mode storage adapters support data retention by using NAND flash memory down on the adapter, backed up by a CacheVault<sup>™</sup> Power Module 05 (CVPM05).

The CVPM05 module is a super-capacitor pack that provides power for the backup of your data in case of host power loss or server failure. The CVPM05 module connects to the controller remotely by cable. The data is backed up to the NAND flash memory available on the MegaRAID storage adapter.

#### **NOTE**

If you do not use the remote mount board or clip included with the CacheVault kit, do not damage the CVPM05 module when mounting in the system. For more information about mounting the CVPM05 module, refer to CVPM02, CVPM05 Power Modules | CVFM04 Cache Module MegaRAID CacheVault Protection Products User Guide.

In the event of host power loss or server failure, any data available in the cache is offloaded to the onboard NAND memory. During this process, the CVPM05 power module powers the necessary components needed for offload.

#### **NOTE**

You cannot hot plug CVPM05 modules. Removing or inserting a CVPM05 module with the adapter powered on might damage the board and the super-capacitor functionality. To attach or remove a CVPM05 module from an adapter, you must fully power down the adapter before you attach the module to or remove the module from its mating connector.

For more information about installation of the CVPM05 module, visit <a href="http://www.broadcom.com/support/download-search">http://www.broadcom.com/support/download-search</a> to download the CacheVault Power Module 05 Getting Started Guide.

# **Adapter Security**

The adapters provide two security features to protect your system from malicious activity:

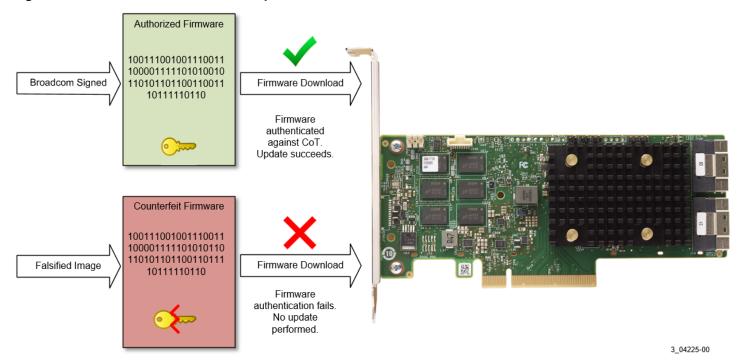
- Hardware secure boot Permits only authenticated firmware to execute on the adapter
- Device authentication Enables another entity in the server to authenticate the adapter

The following sections provide details about each security feature.

#### **Hardware Secure Boot**

Hardware secure boot permits only authenticated firmware to execute on the adapter. The adapter boots from an internal boot ROM, which establishes the initial root of trust (RoT). Hardware secure boot authenticates and builds a chain of trust (CoT) with succeeding firmware images by using the RoT, meaning only authorized firmware executes on the adapter.

Figure 2: Authenticated Firmware Example



Hardware secure boot requires that each image be signed with a valid digital signature; otherwise, the image is considered invalid and does not execute. The adapter ships with a valid signed firmware image. All Broadcom-supplied firmware includes a valid digital signature; therefore, the hardware secure boot process is transparent unless the adapter encounters a counterfeit image. If the adapter downloads a counterfeit image, the image authentication fails and the download utility, such as StorCLI2, displays the appropriate failure messages. Contact Broadcom Technical Support for assistance.

## **Device Authentication**

Device authentication allows the adapter to prove its unique device identity to another entity in the server as part of a platform attestation implementation. The adapter proves its unique device identity with a device ID certificate and a challenge protocol.

The device authentication process includes a platform RoT device (a baseboard management controller [BMC] or a discreet component) and the adapter (the attested device). The platform RoT device requests the device certificate from the controller on the adapter for authentication. If the device authentication process fails, the platform RoT device operates in accordance with its platform security policy.

#### **Device Certificate**

The controller on the adapter uses a device certificate and associated certificate chain to present evidence of its device identity. The certificate chain is based on the X.509 v3 standards and the Security Protocol and Data Model (SPDM) Specification, version 1.1.

The device certificate contains identifying information about the controller, including the device serial number. The private key of a parent and intermediate signing certificate signs the device certificate. The device certificate cannot be modified after manufacture.

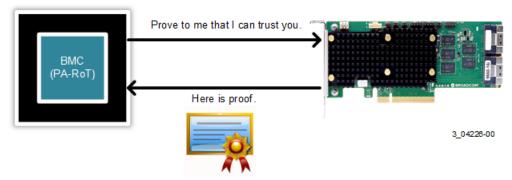
Broadcom manufacturing provisions each board with its device certificate. Every 96xx adapter manufactured is provisioned with a certificate chain. When the SPDM GET\_CERTIFICATE command queries the controller, the controller returns the device certificate chain, which includes a hash of the root certificate.

## **Attestation Procedure**

Attestation is the process in which the server's BMC, or other discrete logic, challenges the adapter for proof of authenticity.

Using attestation in the server is optional. The adapter functions without performing attestation. If attestation fails, the adapter continues to function normally. You must determine the next steps for your system if the adapter fails attestation.

**Figure 3: Attestation Procedure Example** 



To support attestation, you must obtain the external root certificate authority to configure the platform RoT, such as BMC. You can download the external root certificate from Support Documents and Downloads.

## **SPDM Capabilities**

Security Protocol and Data Model (SPDM) Specification v1.1 enables the Requester (BMC/discrete logic) and the Responder (adapter) to exchange keys to enable encryption support for the management interface information exchange. By default, if the Requester asks, the adapter enables authenticated encryption of the management interface. This process occurs dynamically between the Requester and Responder as part of the attestation procedure and requires no change to the adapter's settings.

The following table lists the SPDM v1.1 endpoint CAPABILITIES response message flags. Flags listed as supported respond to the GET\_CAPABILITIES request message.

**Table 19: SPDM CAPABILITIES Response Message Support** 

CAPABILITIES Response Flags Field	Value
CACHE_CAP	0
CERT_CAP	1
CHAL_CAP	1
MEAS_CAP	10
MEAS_FRESH_CAP	0
ENCRYPT_CAP	1
MAC_CAP	1
MUT_AUTH_CAP	1
KEY_EX_CAP	1
PSK_CAP	00
ENCAP_CAP	1
HBEAT_CAP	1
KEY_UPD_CAP	1
HANDSHAKE_IN_THE_CLEAR_CAP	0/1
PUB_KEY_ID_CAP	0

The adapter supports SPDM mutual authentication. The HANDSHAKE\_IN\_THE\_CLEAR\_CAP capability bit is shown as 0/1 because the value is subject to negotiation. The firmware sets the HANDSHAKE\_IN\_THE\_CLEAR\_CAP bit to 0, but the firmware can set the bit to 1 if the Requester sets the bit to 1. While the adapter supports all capabilities listed in the previous table, the Requester might not support the same capabilities. The adapter correctly negotiates its capabilities with that of the Requester. Refer to the *StorCL12 Utility User Guide* for additional security command information.

# **Adapter Installation**

Use the steps in this section to install the Broadcom adapter.

#### 1. Unpack your adapter.

Unpack and remove the adapter. Inspect the adapter for damage. If it appears damaged, contact Broadcom Technical Support.

#### **ATTENTION**

To avoid the risk of data loss, back up your data before you change your system configuration.

#### 2. Turn off the power to the system.

Turn off the power to the computer and disconnect the AC power cord. Remove the computer cover. Refer to the system documentation for instructions. Before you install the adapter, make sure that the computer is disconnected from the power and from any networks.



#### **CAUTION**

Disconnect the computer from the power supply and from any networks to which you will install the adapter, or you risk damaging the system or experiencing electrical shock.

- 3. **Review the adapter connectors.** See Broadcom MegaRAID and HBA Tri-Mode Storage Adapter Characteristics for descriptions of the adapters that show their connectors.
- 4. Check the mounting bracket on the adapter.

If required for your system, replace the full-profile mounting bracket that ships on the adapter with the low-profile bracket supplied. Complete the following steps to attach the low-profile bracket:

- a) Using a No. 1 Phillips screwdriver that is ESD safe, remove the two Phillips screws that connect the full-profile bracket to the board. Unscrew the two screws located at the top and bottom edges of the board. Avoid touching any board components with the screwdriver or the bracket.
- b) Remove the full-profile bracket. Do not damage the adapter.
- c) Place the adapter on top of the low-profile bracket. Position the bracket so that the screw holes in the tabs align with the openings in the board.
- d) Using a No. 1 Phillips torque screwdriver that is ESD safe, set to a maximum torque of 4.8 ±0.5 inch-pounds. Replace the two Phillips screws removed in Step a.

#### **ATTENTION**

Exceeding this torque specification can damage the board, connectors, or screws, and can void the warranty on the board.

#### **ATTENTION**

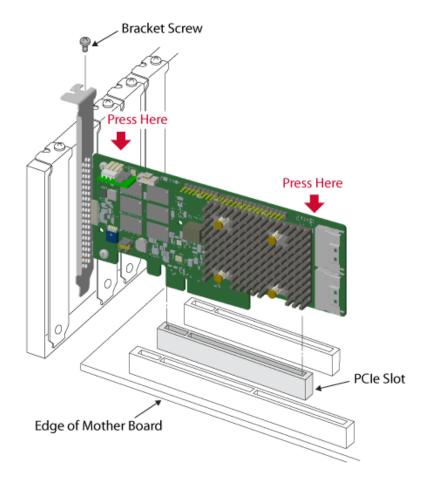
Damage caused to the board as a result of changing the bracket can void the warranty on the board. Adapters returned without a bracket mounted on the board will be sent back without return merchandise authorization (RMA) processing.

5. **Insert the adapter into an available PCIe slot.** Select a PCIe slot, and align the adapter's PCIe bus connector to the slot, as shown in the following figure. Press down gently, but firmly, to make sure that the adapter is seated correctly in the slot. Secure the bracket to the computer chassis with the bracket screw.

#### NOTE

Adapters with a x8 host interface can operate in x8 or x16 slots. However, some x16 PCle slots support only PCle graphics cards; an adapter installed in one of these slots will not function. Refer to the guide for your motherboard for information about the PCle slots.

Figure 4: Installing an Adapter in a PCIe Slot



6. Configure and install the SAS, SATA, or PCIe (NVMe) devices in the host computer case.

Refer to documentation for the devices for any preinstallation configuration requirements.

7. **Connect the adapter to the devices.** Connect the appropriate cable with the connectors on one end to connect to the adapter and the appropriate connector on the other end to attach to the backplane connector.

The maximum cable length is 1 meter (39.37 in.). A single wide-port SAS or multilane PCIe (NVMe) device cannot connect to phys controlled by different CSWs. See Tri-Mode Storage Interface for more information.

- 8. Provide the required airflow for the installed adapter. See #unique\_36 to find the adapter's cooling requirements.
- 9. **Turn on the power to the system.** Reinstall the computer cover, and reconnect the AC power cords. Make sure that the power is turned on to the storage devices before or at the same time that the power is turned on to the host

computer. Turn on power to the host computer. If the computer is powered on before these devices, the devices might not be recognized.

During boot, a BIOS message appears. The firmware takes several seconds to initialize. The configuration utility prompt times out after several seconds. The second portion of the BIOS message shows the adapter controller number, firmware version, and cache SDRAM size. The numbering of the adapters follows the PCIe slot scanning order used by the host motherboard.

- 10. **Choose the correct storage profile.** Refer to the *MegaRAID Tri-Mode Software User Guide* and *LSI*<sup>®</sup> *Storage Authority Software User Guide* for details about setting up your adapter.
- 11. Install the operating system driver. The adapters can operate under various operating systems. To operate under these operating systems, you must install the software drivers. The firmware and drivers are routinely updated and made available on the Support and Download center. Visit <a href="http://www.broadcom.com/support/download-search">http://www.broadcom.com/support/download-search</a>, and download the latest firmware and driver for the adapter.

The hardware installation of your adapter is complete.

# **Broadcom MegaRAID and HBA Tri-Mode Storage Adapter Characteristics**

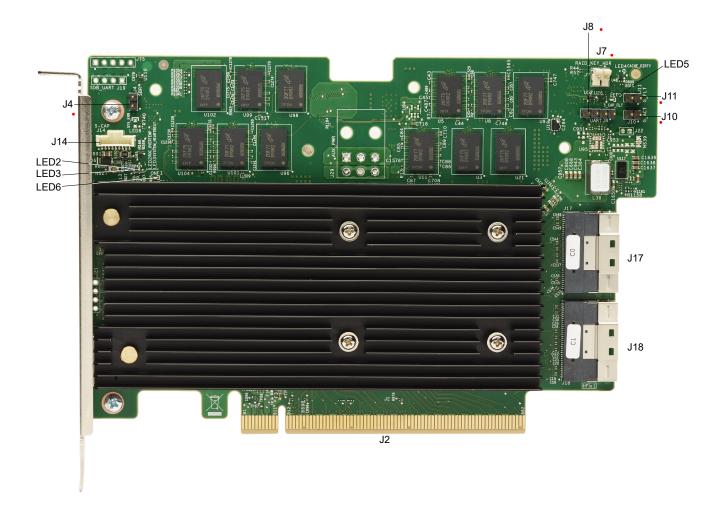
This chapter presents the physical characteristics of each board, including the board size and the connector locations.

## MegaRAID 9670W-16i Adapter – Connector and LED Designations

The adapter is a 167.52  $(\pm 0.13)$  mm × 111.15  $(\pm 0.13)$  mm board. The component height on the top and bottom of the adapter complies with the PCIe specification.

The following figure shows the connectors and LED locations on the adapter. Pin 1 on the headers and connectors is highlighted in red in the figure.

Figure 5: Card Layout for the MegaRAID 9670W-16i Tri-Mode Storage Adapter



The following table describes the headers and connectors on the adapter.

**Table 20: Headers and Connectors** 

Connector	Туре	Description
J2	Standard edge card connector	The interface between the storage adapter and the host system.  With the PCIe interface, this connector provides power to the board and an I <sup>2</sup> C interface connected to the I <sup>2</sup> C bus for the Intelligent Platform Management Interface (IPMI).
J4	Default SBR header	2-pin connector. Reserved for Broadcom use.
J7	Advanced software options hardware key header	2-pin connector. Enables support for selected advanced features.
J8	Onboard serial UART connector	4-pin connector. Reserved for Broadcom use.
J10	Global HDD activity LED header	2-pin connector.  Connects to an LED that indicates activity on the drives connected to the adapter.
J11	Global drive fault LED header	2-pin connector. Connects to an LED that indicates whether a drive is in a fault condition.
J14	CacheVault power module interface	9-pin connector. Connects the adapter to a CacheVault power module.
J17, J18	Storage interface connectors	Two SFF-8654 8-port internal connectors. Connect the adapter by cable to the storage devices.

The following table describes the LEDs on the adapter.

**Table 21: LED Designations** 

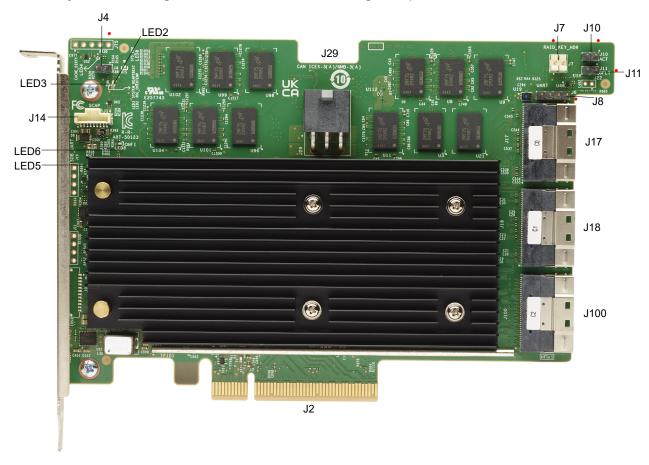
LED	Туре	Description
LED2	Yellow controller overtemperature	Stays on solid to indicate that the SAS4116W RoC temperature sensor is over the temperature threshold. When the device is in the proper temperature range, this LED is off.
LED3	Green system heartbeat	Indicates that the SAS4116W RoC ASIC is operating normally. This LED blinks at 1 Hz.
LED5	Yellow supercap fault	Indicates that the CacheVault power module is in fault state or is overtemperature. When the energy pack is in the FAULT condition or is missing, this LED is on.
LED6	Green ONFI activity	Indicates when the ONFI is active for cache offload or recovery.

# MegaRAID 9670-24i Adapter - Connector and LED Designations

The adapter is a 167.65 ( $\pm 0.13$ ) mm  $\times$  111.15 ( $\pm 0.13$ ) mm board. The component height on the top and bottom of the adapter complies with the PCIe specification.

The following figure shows the connectors and LED locations on the adapter. Pin 1 on the headers and connectors is highlighted in red in the figure.

Figure 6: Card Layout for the MegaRAID 9670-24i Tri-Mode Storage Adapter



The following table describes the headers and connectors on the adapter.

**Table 22: Headers and Connectors** 

Connector	Туре	Description
J2	Standard edge card connector	The interface between the storage adapter and the host system.  With the PCIe interface, this connector provides power to the board and an I <sup>2</sup> C interface connected to the I <sup>2</sup> C bus for the Intelligent Platform Management Interface (IPMI).
J4	Default SBR header	2-pin connector. Reserved for Broadcom use.
J7	Advanced software options hardware key header	2-pin connector. Enables support for selected advanced features.
J8	Onboard serial UART connector	4-pin connector. Reserved for Broadcom use.
J10	Global HDD activity LED header	2-pin connector.  Connects to an LED that indicates activity on the drives connected to the adapter.
J11	Global drive fault LED header	2-pin connector.  Connects to an LED that indicates whether a drive is in a fault condition.

Connector	Туре	Description
J14	CacheVault power module interface	9-pin connector. Connects the adapter to a CacheVault power module.
J17, J18, J100	Storage interface connectors	Three SFF-8654 8-port internal connectors. Connect the adapter by cable to the storage devices.
J29	Auxiliary power connector	6-pin connector.

The following table describes the LEDs on the adapter.

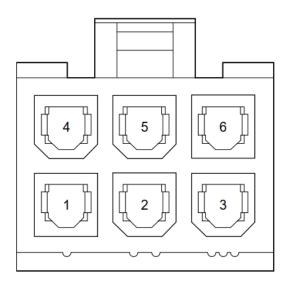
**Table 23: LED Designations** 

LED	Туре	Description
LED 2	Yellow controller overtemperature	Stays on solid to indicate that the SAS4124 RoC temperature sensor is over the temperature threshold. When the device is in the proper temperature range, this LED is off.
LED 3	Green system heartbeat	Indicates that the SAS4124 RoC ASIC is operating normally. This LED blinks at 1 Hz.
LED 5		Indicates that the CacheVault power module is in fault state or is overtemperature. When the energy pack is in the FAULT condition, this LED is on.
LED 6	Green ONFI activity	Indicates when the ONFI is active for cache offload or recovery.

## **Auxiliary Power Connector**

The MegaRAID 9670-24i adapter supports the *PCI Express Card Electromechanical Specification* defined 2×3 auxiliary power connector. Connect the auxiliary power connector if the slot cannot supply the required power as defined in MegaRAID Tri-Mode Storage Adapter Power Supply Requirements. The power check algorithm detects if auxiliary power is connected to the adapter. If auxiliary power is connected, the power check is bypassed and the card is fully enabled. The auxiliary power connector's pin definition meets the PCIe CEM specification and the following table is included for reference. The following figure shows the connector's pin designations.

**Figure 7: Auxiliary Power Connector** 



#### **Table 24: Auxiliary Power Connector Pin Definition**

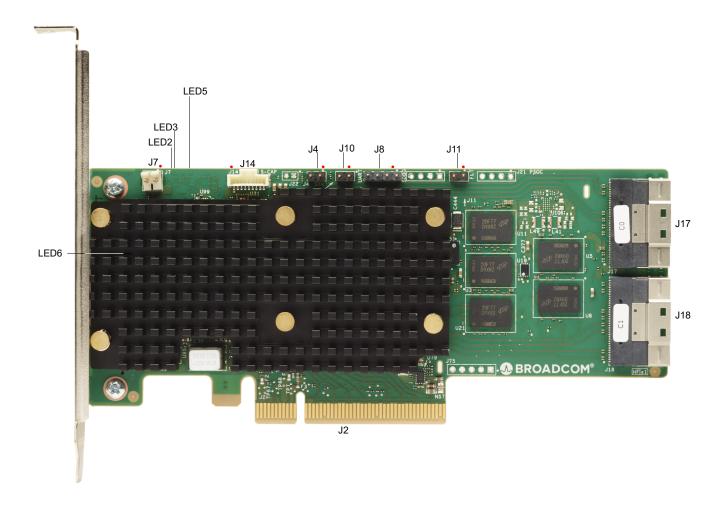
Pin	Signal
1	+12V
2	+12V
3	+12V
4	Ground
5	Sense
6	Ground

# MegaRAID 9660-16i Adapter - Connector and LED Designations

The adapter is a 155.52 ( $\pm 0.13$ ) mm × 68.77 ( $\pm 0.13$ ) mm board. The component height on the top and bottom of the adapter complies with the PCIe specification.

The following figure shows the connectors and LED locations on the adapter. Pin 1 on the headers and connectors is highlighted in red in the figure.

Figure 8: Card Layout for the MegaRAID 9660-16i Tri-Mode Storage Adapter



The following table describes the headers and connectors on the adapter.

**Table 25: Headers and Connectors** 

Connector	Туре	Description
J2	Standard edge card connector	The interface between the storage adapter and the host system.  With the PCIe interface, this connector provides power to the board and an I <sup>2</sup> C interface connected to the I <sup>2</sup> C bus for the Intelligent Platform Management Interface (IPMI).
J4	Default SBR header	2-pin connector. Reserved for Broadcom use.
J7	Advanced software options hardware key header	2-pin connector. Enables support for selected advanced features.
J8	Onboard serial UART connector	4-pin connector. Reserved for Broadcom use.
J10	Global HDD activity LED header	2-pin connector.  Connects to an LED that indicates activity on the drives connected to the adapter.

Connector	Туре	Description
J11	Global drive fault LED header	2-pin connector.  Connects to an LED that indicates whether a drive is in a fault condition.
J14	CacheVault power module interface	9-pin connector. Connects the adapter to a CacheVault power module.
J17, J18	Storage interface connectors	Two SFF-8654 8-port internal connectors. Connect the adapter by cable to the storage devices.

The following table describes the LEDs on the adapter. These LEDs reside on the nonheat-sink side of the board.

### **Table 26: LED Designations**

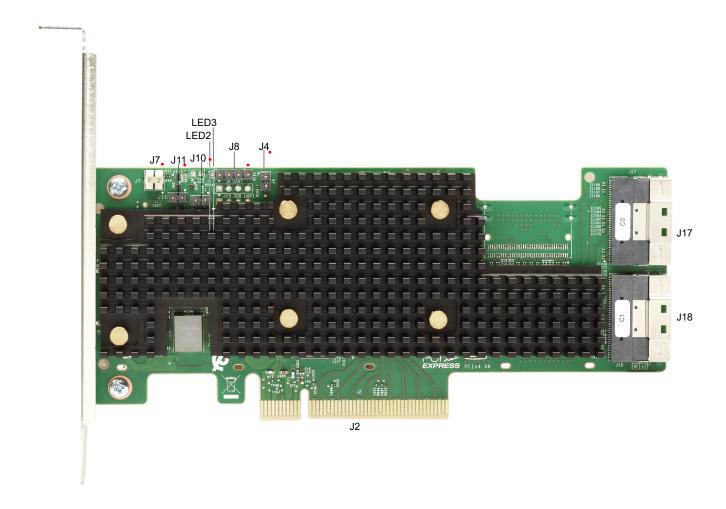
LED	Туре	Description
LED2	Yellow controller overtemperature	Stays on solid to indicate that the SAS4116 device temperature sensor is over the temperature threshold. When the device is in the proper temperature range, this LED is off.
LED3	Green system heartbeat	Indicates that the SAS4116 RoC ASIC is operating normally. This LED blinks at 1 Hz.
LED5	Yellow supercap fault	Indicates that the CacheVault power module is in fault state or is overtemperature. When the energy pack is in the FAULT condition or is missing, this LED is on.
LED6	Green ONFI activity	Indicates when the ONFI is active for cache offload or recovery.

# eHBA 9620-16i Adapter - Connector and LED Designations

The adapter is a 155.52 ( $\pm 0.13$ ) mm  $\times$  68.77 ( $\pm 0.13$ ) mm board. The component height on the top and bottom of the adapter complies with the PCIe specification.

The following figure shows the connectors and LED locations on the adapter. Pin 1 on the headers and connectors is highlighted in red in the figure.

Figure 9: Card Layout for the eHBA 9620-16i Storage Adapter



The following table describes the headers and connectors on the adapter.

**Table 27: Headers and Connectors** 

Connector	Туре	Description
J2	Standard edge card connector	The interface between the storage adapter and the host system.  With the PCle interface, this connector provides power to the board and an I <sup>2</sup> C interface connected to the I <sup>2</sup> C bus for the Intelligent Platform Management Interface (IPMI).
J4	Default SBR header	2-pin connector. Reserved for Broadcom use.
J8	Onboard serial UART connector	4-pin connector. Reserved for Broadcom use.
J10	Global HDD activity LED header	2-pin connector. Connects to an LED that indicates activity on the drives connected to the adapter.
J11	Global drive fault LED header	2-pin connector. Connects to an LED that indicates whether a drive is in a fault condition.

Connector	Туре	Description
J17, J18	Storage interface connectors	Two SFF-8654 8-port internal connectors.
		Connect the adapter by cable to the storage devices.

The following table describes the LEDs on the adapter.

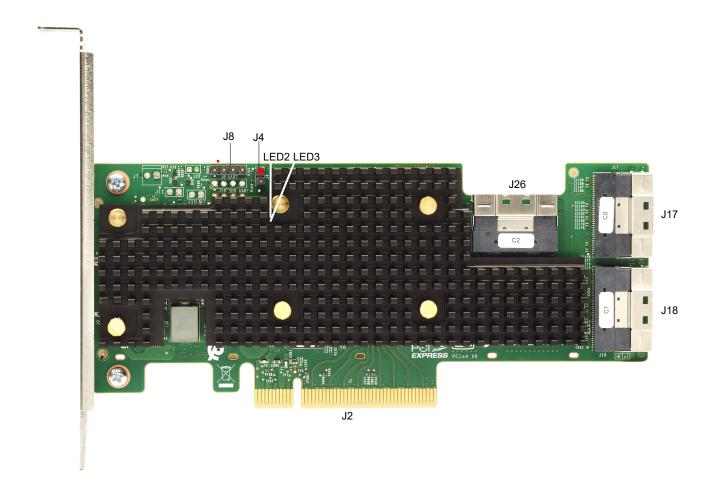
**Table 28: LED Designations** 

LED	Туре	Description
LED2	Yellow controller overtemperature	Stays on solid to indicate that the SAS4116 device temperature sensor is over the temperature threshold. When the device is in the proper temperature range, this LED is off.
LED3	Green system heartbeat	Indicates that the SAS4116 RoC ASIC is operating normally. This LED blinks at 1 Hz.

# eHBA 9600-24i Adapter - Connector and LED Designations

The adapter is a 155.52 ( $\pm 0.13$ ) mm × 68.77 ( $\pm 0.13$ ) mm board. The component height on the top and bottom of the adapter complies with the PCIe specification.

Figure 10: Card Layout of the eHBA 9600-24i Tri-Mode Storage Adapter



The following table describes the connectors on the adapter.

**Table 29: Headers and Connectors** 

Connector	Туре	Description
J2	Standard edge card connector	The interface between the storage adapter and the host system.  With the PCIe interface, this connector provides power to the board and an I <sup>2</sup> C interface connected to the I <sup>2</sup> C bus for the IPMI.
J4	Default SBR header	2-pin connector. Reserved for Broadcom use.
J8	Onboard serial UART connector	4-pin connector. Reserved for Broadcom use.
J17, J18, J26	Storage interface connectors	Three SFF-8654 8-port internal connectors. Connect the adapter by cable to the storage devices.

The following table describes the LEDs on the adapter.

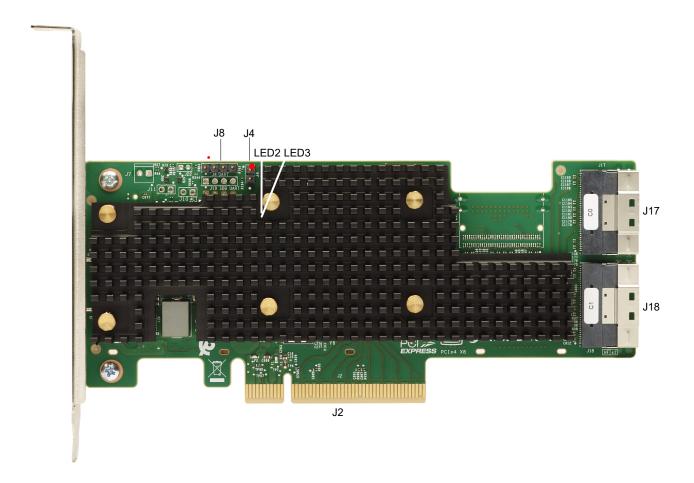
**Table 30: LED Designations** 

LED	Туре	Description
LED2	-	Stays on solid to indicate that the SAS4024 IOC temperature sensor is over the temperature threshold. When the device is in the proper temperature range, this LED is off. This LED resides on the nonheat-sink side of the board.
LED3	Green system heartbeat	Indicates that the SAS4024 IOC is operating normally. This LED resides on the nonheat-sink side of the board.

# eHBA 9600-16i Adapter - Connector and LED Designations

The adapter is a 155.52 ( $\pm 0.13$ ) mm × 68.77 ( $\pm 0.13$ ) mm board. The component height on the top and bottom of the adapter complies with the PCIe specification.

Figure 11: Card Layout of the eHBA 9600-16i Tri-Mode Storage Adapter



The following table describes the connectors on the adapter.

**Table 31: Headers and Connectors** 

Connector	Туре	Description
J2	Standard edge card connector	The interface between the storage adapter and the host system.  With the PCIe interface, this connector provides power to the board and an I <sup>2</sup> C interface connected to the I <sup>2</sup> C bus for the IPMI.
J4	Default SBR header	2-pin connector. Reserved for Broadcom use.
J8	Onboard serial UART connector	4-pin connector. Reserved for Broadcom use.
J17, J18	Storage interface connectors	Two SFF-8654 8-port internal connectors. Connect the adapter by cable to the storage devices.

The following table describes the LEDs on the adapter.

**Table 32: LED Designations** 

LED	Туре	Description
LED2		Stays on solid to indicate that the SAS4016 IOC temperature sensor is over the temperature threshold. When the device is in the proper temperature range, this LED is off. This LED resides on the nonheat-sink side of the board.
LED3	Green system heartbeat	Indicates that the SAS4016 IOC is operating normally. This LED resides on the nonheat-sink side of the board.

# eHBA 9600-8i8e Adapter - Connector and LED Designations

The adapter is a 167.51 ( $\pm 0.13$ ) mm  $\times$  68.78 ( $\pm 0.13$ ) mm board. The component height on the top and bottom of the adapter complies with the PCIe specification.

The following figure shows the connectors and LED locations on the adapter. A red circle near each header and connector identifies pin 1 in the figure.

Figure 12: Card Layout of the eHBA 9600-8i8e Tri-Mode Storage Adapter



The following table describes the connectors on the adapter.

**Table 33: Headers and Connectors** 

Connector	Туре	Description
J2	Standard edge card connector	The interface between the storage adapter and the host system.  With the PCle interface, this connector provides power to the board and an I <sup>2</sup> C interface connected to the I <sup>2</sup> C bus for the IPMI.
J4	Default SBR header	2-pin connector. Reserved for Broadcom use.
J8	Onboard serial UART connector	4-pin connector. Reserved for Broadcom use.
J26	Storage interface connector	One SFF-8654 8-port internal connector. Connect the adapter by cable to the storage devices.
J163, J28	Storage interface connectors	Two SFF-8674 4-port external connectors.

The following table describes the LEDs on the adapter.

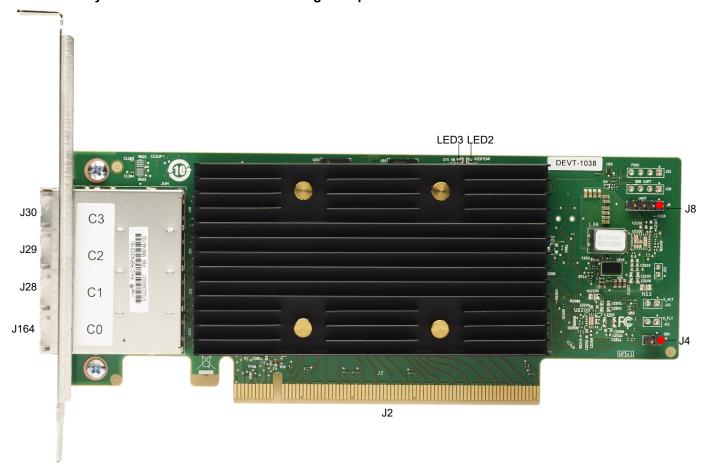
**Table 34: LED Designations** 

LED	Туре	Description
LED2	-	Stays on solid to indicate that the SAS4016 IOC temperature sensor is over the temperature threshold. When the device is in the proper temperature range, this LED is off. This LED resides on the nonheat-sink side of the board.
LED3	Green system heartbeat	Indicates that the SAS4016 IOC is operating normally. This LED resides on the nonheat-sink side of the board.

# eHBA 9600W-16e Adapter - Connector and LED Designations

The adapter is a 167.51 ( $\pm 0.13$ ) mm  $\times$  68.78 ( $\pm 0.13$ ) mm board. The component height on the top and bottom of the adapter complies with the PCIe specification.

Figure 13: Card Layout of the eHBA 9600W-16e Storage Adapter



The following table describes the headers and connectors on the adapter.

**Table 35: Connectors** 

Connector	Туре	Description
J2	Standard board edge connector	PCIe x8 board edge connector.  With the PCIe interface, this connector provides power to the board and an I <sup>2</sup> C interface connected to the I <sup>2</sup> C bus for the IPMI.
J4	Default SBR header	2-pin connector. Reserved for Broadcom use.
J8	Onboard serial UART connector	4-pin connector. Reserved for Broadcom use.
J164, J28, J29, J30	Storage interface connectors	Four SFF-8674 external connectors. Connect the adapter by cable to the storage devices.

The following table describes the LEDs on the adapter.

**Table 36: LED Designations** 

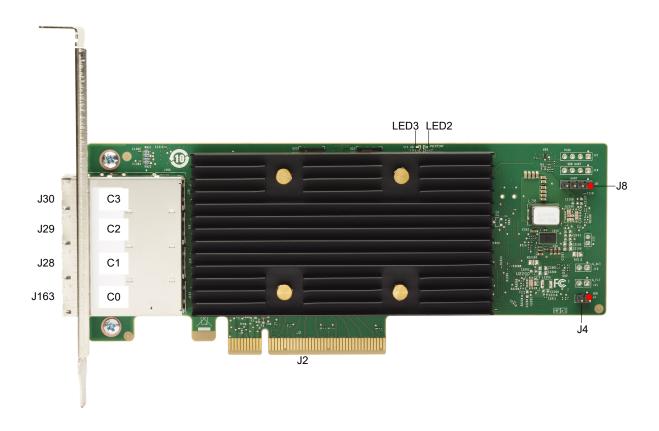
LED	Туре	Description
LED2		Stays on solid to indicate that the SAS4016W IOC temperature sensor is over the temperature threshold. When the device is in the proper temperature range, this LED is off.
LED3	Green system heartbeat	Indicates that the SAS4016W IOC is operating normally.

# eHBA 9600-16e Adapter - Connector and LED Designations

The adapter is a 167.51 ( $\pm 0.13$ ) mm  $\times$  68.77 ( $\pm 0.13$ ) mm board. The component height on the top and bottom of the adapter complies with the PCIe specification.

The following figure shows the connectors and LED locations on the adapter. A red circle near each header and connector identifies pin 1 in the figure.

Figure 14: Card Layout of the eHBA 9600-16e Storage Adapter



The following table describes the headers and connectors on the adapter.

**Table 37: Connectors** 

Connector	Туре	Description
J2	-	PCIe x8 board edge connector.  With the PCIe interface, this connector provides power to the board and an I <sup>2</sup> C interface connected to the I <sup>2</sup> C bus for the IPMI.
J4	Default SBR header	2-pin connector. Reserved for Broadcom use.
J8	Onboard serial UART connector	4-pin connector. Reserved for Broadcom use.
J30, J29, J28, J163	]	Four SFF-8674 external connectors. Connect the adapter by cable to the storage devices.

The following table describes the LEDs on the adapter.

## **Table 38: LED Designations**

LED	Туре	Description
LED2		Stays on solid to indicate that the SAS4016 IOC temperature sensor is over the temperature threshold. When the device is in the proper temperature range, this LED is off.
LED3	Green system heartbeat	Indicates that the SAS4016 IOC is operating normally.

# Tri-Mode StorageAdapter Technical Specifications

This chapter presents the technical specifications of each board, including operating conditions and power supply requirements.

## **Board Storage Conditions**

The following table lists the board storage conditions for the storage adapters. The minimum airflow, measured as linear feet per minute (LFM) at 55°C, must be met to avoid operating the controller's processor and board components above their maximum junction temperatures.

**Table 39: Board Storage Conditions** 

Adapter	Relative Humidity Range (Non-condensing)	Minimum LFM	Temperature Range <sup>a</sup>
9670W-16i	10% to 90%	200	-40°C to +70°C
9670-24i	10% to 90%	200	-40°C to +70°C
9660-16i	10% to 90%	250	-40°C to +70°C
9620-16i	10% to 90%	150	-40°C to +70°C
9600-24i	10% to 90%	150	-40°C to +70°C
9600-16i	10% to 90%	150	-40°C to +70°C
9600-8i8e	10% to 90%	200	-40°C to +70°C
9600W-16e	10% to 90%	200	-40°C to +70°C
9600-16e	10% to 90%	200	-40°C to +70°C

## Tri-Mode Storage Adapter Power Supply Requirements

All power is supplied to the tri-mode storage adapter through the PCle 3.3V rails (3.3V ±9%) and the 12V rail (12V ±8%). Onboard switching regulator circuitry operates from the 3.3V rails and the 12V rail provides the necessary voltages.

Typical power is measured with maximum I/O traffic, typical silicon process material, and nominal voltages operating the card at an ambient temperature of 45°C with required airflow.

The system might transmit a PCI Set Slot Power Limit Message that indicates the maximum amount of power that the adapter might use. During the start-up procedure, the adapter's power consumption is limited to 25W because the storage phys are not enabled until the Auto Port Enable procedure begins. When this procedure begins, the adapter's power check algorithm compares the power allocated by the SET\_SLOT\_POWER\_LIMIT message to the adapter's slot power requirement. If the SET\_SLOT\_POWER\_LIMIT message indicates that the slot's power budget is at or greater than the adapter's requirement, Auto Port Enable proceeds. Refer to SAS95xx Adapters and SAS96xx Adapters Slot Power Limit Settings Specification for more information about slot power requirements.

If no SET\_SLOT\_POWER\_LIMIT message is received by the time Auto Port Enable is to start, the adapter continues with port enable. You must make sure enough power is available to the slot if the SET\_SLOT\_POWER\_LIMIT message is not used. For adapters with auxiliary power connectors, the slot power check is bypassed if the auxiliary power connector is connected. For more information about the auxiliary power connector, see Auxiliary Power Connector.

a. For adapters with a CVPM, this range applies with or without the CVPM attached.

## MegaRAID Tri-Mode Storage Adapter Power Supply Requirements

The following table describes the typical power consumption of the MegaRAID adapters.

Table 40: MegaRAID Tri-Mode Storage Adapter Typical Power Consumption

Adapter	Typical Power (W)
9670W-16i	28
9670-24i	28
9660-16i	20

During the transparent learn cycle, the CacheVault power module consumes up to an additional 8W. The PCle 3.3V rail supplies the power for the learn cycle.

## eHBA Tri-Mode Storage Adapter Power Supply Requirements

The following table describes the typical power consumption of the eHBAs.

Table 41: eHBA Tri-Mode Storage Adapter Typical Power Consumption

Adapter	Typical Power (W)
9620-16i	17
9600-24i	20
9600-16i	17
9600-8i8e	17
9600W-16e	20
9600-16e	17

## Overtemperature Behavior

The adapter supports the following temperature threshold events to alert of overtemperature situations when sufficient airflow is not provided. If supported, the system can use the events generated in the Persistent Event Log (PEL) to manage fan speed and mitigate overtemperature conditions. While these events assist in managing overtemperature conditions, potential damage can occur if sufficient airflow is not provided to the adapter.

**Table 42: Temperature Threshold Events** 

Condition	RoC or IOC Junction Temperature (°C)	Result
Warning	105	Generate a PEL event.
Critical	107	Generate a PEL event and decrease device-side ports to their slowest operating speed. The junction temperature must return to 104°C, or lower, to resume normal operation.
Fatal	115	Generate a PEL event and fault the adapter. The junction temperature must return to 104°C, or lower, and the adapter must be reset to resume normal operation.

# Marks, Certifications, Compliance, and Safety Characteristics

This section lists the adapter marks and certifications, FCC compliance statements, and safety characteristics.

## Marks, Certifications, and Compliance

The design and implementation of the adapters minimize electromagnetic emissions, susceptibility to radio frequency energy, and the effects of electrostatic discharge.

The following adapters show the marks and certifications included in Table 44, Adapter Marks and Certifications.

**Table 43: Adapter Models** 

Adapter	Model Number
MegaRAID 9670W-16i	50113
MegaRAID 9660-16i	50107
eHBA 9600-24i eHBA 9600-16i HBA 9620-16i	50111
eHBA 9600-8i8e	50145
eHBA 9600W-16e	50108
eHBA 9600-16e	50118

**Table 44: Adapter Marks and Certifications** 

Mark	Symbol	Description
Australia and New Zealand RCM		Meets the following standards:  • AS/NZS CISPR 32  • CISPR 32:2015, Class A  • AS/NZS CISPR 32:2015, Class A
Canada EMC	CANADA ICES-OO3 CLASS A CANADA NMB-003 CLASSE A CAN ICES-3 (A)/NMB-3 (A)	Meets the following standards:  ICES-003:2016 Issue 6: 2016, Class A  CAN/CSA CISPR 22-10  CISPR 22:2008
Europe (CE)	CE	Meets the following standards:  • EN 55024, EN 55032, EN 55035  • EN 55032:2015 +AC:2016, Class A  • EN 55024:2010 +A1:2015  • EN 55035:2017 +A11:2020

Mark	Symbol	Description
Korea (RRL)	R-R-A8T-xxxxx	xxxxx = model number Meets the KN32/KN35 testing requirements.
Taiwan (BSMI)	D3B320 RoHS	Meets the following standards:  CNS 13438  CNS15663
USA / Canada Safety (UL Listed)	C S US  c S US  c S US  E 257743	For use with UL listed ITE equipment only.  Meets the following standards:  UL 62368-1, Second Edition  CAN/CSA C22.2 No. 62368-1-14
CB Scheme Safety	_	Meets the following standards:  • IEC 62368-1:2014 (Second edition)  • EN 62368-1:2014+A11: 2017
Japan (VCCI)	VEI	Meets the following standards:  • V-3/2015.04, Class A  • V-4/2012.04  • VCCI-CISPR 32:2016  • CISPR 32:2015
USA / Canada (FCC)	FC	Meets the following standards:  • 47 CFR FCC Part 15, Subpart B, Class A  • ANSI C63.4:2014  • CISPR 32:2008
Morocco (CMIM)	6	Meets the following standards:  • EN 55024, EN 55032, EN 55035  • EN 55032:2015 +AC:2016, Class A  • EN 55024:2010 +A1:2015  • EN 55035:2017 +A11:2020
Country of Origin	Made in XXXX	XXXX indicates the country of origin.

# **FCC Compliance**

This device complies with part 15 of the FCC rules.

Operation is subject to the following two conditions: (1) this device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

### NOTE

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference, in which case the user is required to correct the interference at his or her own expense.

## **Safety Characteristics**

All tri-mode storage adapters meet or exceed the requirements of UL flammability rating 94 V0. Each bare board is also marked with the supplier name or trademark, type, and UL flammability rating. For the boards installed in a PCIe bus slot, all voltages are lower than the SELV 42.4V limit.

# **Cable Drawings and Pinouts**

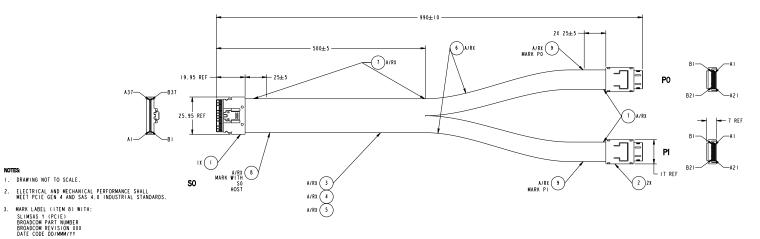
Use the cable drawings and pinouts in this appendix if your design requires you to design your own cables.

## Cable 05-60001-00

The following figure shows the drawing and pinout for Broadcom cable 05-60001-00, a x8 SFF-8654 to 2 x4 SFF-8612 connection.

Figure 15: Cable 05-60001-00 Drawing and Pinout

REVISIONS				
REV	ECO	DESCRIPTION	DATE	APPROVED
001		PRELIMINARY RELEASE	26FEB18	
002	175141	UPDATE WIRE GAUGE AND IMPEDANCE TOLERANCE ITEM 3. REVISE MARKING REQUIREMENTS NOTE 3.	2   NOV   9	



SECONDARY MARKING WITH MANUFACTURER PART NUMBER AND REVISION ACCEPTABLE.

4. SEE CABLE WIRING TABLE AND RELATED NOTES.

9	LABEL	LABEL, 40X14MM, WHITE, R2, HF	A/R	
8	LABEL	LABEL, 70X26MM, WHITE, R2, HF	A/R	SEE NOTE 3
7	TAPE	ACETATE TAPE: W=1 INCH	A/R	
6	SLEEVING	EXPANDO TUBE: OD=7MM, GREEN, VW-I, HF	A/R	
5	SLEEVING	EXPANDO TUBE: OD=10MM, GREEN, VW-I, HF	A/R	
4	WIRE	ULIO61, STRANDED	A/R	SEE NOTE 4
3	CABLE	SAS CABLE: UL20744, 28-32AWG, 92.5Ω ±10% DIFF, SAS4, PCIE GEN 4, VW-1, NATURAL, HF	A/R	
2	CONNECTOR	SFF-8612, 42P, STRAIGHT, ACTIVE LATCH, X4, SAS 4.0, PCIE GEN 4	2	SEE NOTE 4
T	CONNECTOR	SFF-8654, 74P, STRAIGHT, X8, SAS 4.0, PCIE GEN 4	1	SEE NOTE 4
LTFM	CALLOUT	DESCRIPTION	CHANTITY	NOTES

APPROVALS	DATE	UNLESS OTHERWISE DIMENSIONS ARE IN			A BROAL	200	л•
DRAWN BY	TOLERANCES: X,X ± 0,2 MM		₩ BROADCOM*				
APPROVED BY	X.XX ± 0.15 M	AM O MM	TITLE	CABLE, SFF-8		2 X	
Company Confidential O Copyright All Rights Reserved.Any copy is on		ANGLES ± 1°			SFF-8612 X	4, PCIE,	I M
The passessor is responsible for a document's revision is current, possessor is responsible for re documents from their poin	1.500	⊕€]	S12E	5067-6865	002	SHEET	

CONNECTOR PIN/PAD CONNECTOR SIGNAL WIRE/CABLE NOTES

CONNECTOR SIGNAL	CONNECTOR	PIN/PAD		CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE	NOTES
NC	SO_			P0	BI	NC		
GND	HOST	Al	-	BACKPLANE	82	GND	ITEM 3	
R x 0 +		A2	Н—	-	B3	T x 0 +	ITEM 3	
R x 0 -		A3	H		84	T x 0 -	ITEM 3	
GND		A4	HX—		B5	GND	ITEM 3	
RxI+		A5	Н	-	B6	Tx1+	ITEM 3	
RxI-		A6	H <i>)</i> –	-	87	TxI-	ITEM 3	
GND		A7	$\vdash$	-	B8	GND	ITEM 3	
SB7A		A8				NC		NOTE 2
SB4A		A 9	_		BIO	CWAKE#	ITEM 4	NOTE I
SB3A		A10	-	-	B9	CBL_ID	ITEM 4	NOTE I
SB3A		AIO	-	-	BII	GND	ITEM 3	
SBA+		All	Н—		B12	REFCLK+	ITEM 3	
SBA-		A12	H	-	BI3	REFCLK-	ITEM 3	
GND	1	AI3	HX—	-	B14	GND	ITEM 3	
Rx2+		A14	Н	-	BI5	Tx2+	ITEM 3	
Rx2-		A15	H	-	B16	Tx2-	ITEM 3	
GND		A16	HX—		B17	GND	ITEM 3	
Rx3+		A17	H \	-	B18	T x 3+	ITEM 3	
Rx3-		AI8	Н —		B19	Tx3-	ITEM 3	
GND		AI9	$\vdash$	-	B20	GND	ITEM 3	
NC					B21	NC		
NC					Al	NC		
GND		BI	-	-	A2	GND	ITEM 3	
Tx0+		B2	Н —	-	A3	R x 0 +	ITEM 3	
Tx0-		B3	Н —	-	A4	R x 0 -	ITEM 3	
GND		B4	HX —	-	A5	GND	ITEM 3	
Tx1+		B5	Н—		A6	RxI+	ITEM 3	
Tx1-		B6	H	-	A7	RxI-	ITEM 3	
GND		B7	$\vdash$ $\vdash$	-	A8	GND	ITEM 3	
SBOA		B8	_		A 9	SCL	ITEM 4	NOTE I
SBIA		B9	-	-	AIO	SDA	ITEM 4	NOTE I
SB2A		BIO	$\vdash$		All	GND	ITEM 3	
SB5A		BII	Η	-	A12	PERST#	ITEM 3	
SB6A		B12	H		A13	D_INPL#	ITEM 3	
GND		BI3	HX-		AI4	GND	ITEM 3	
Tx2+		B14	H+		A15	RX2+	ITEM 3	
Tx2-		B15	Н/		A16	R x 2 -	ITEM 3	
GND		B16	Ж		A17	GND	ITEM 3	
Tx3+		B17	H+		A18	R x 3+	ITEM 3	
Tx3-		B18	H		A19	R x 3 -	ITEM 3	
GND		B19	$\vdash$		A20	GND	ITEM 3	
NC					A21	NC		

BACKPLANE   R144   A20   A21   A21   A22   A23   B3   T104   TEM 3   B4   T107   TEM 3   B6   T114   TEM 3   B7   T111   TEM 3   B7   T111   TEM 3   B8   GMD   TEM 3   B8   GMD   TEM 3   B8   GMD   TEM 3   B10   CMARCE   TIEM 4   NOTE 1   B10   GMD   TEM 3   B10   CMARCE   TIEM 4   NOTE 1   B11   GMD   TEM 3   B11   GMD   TEM 3   B12   REFCLEK   TIEM 3   B13   REFCLEK   TIEM 3   B14   GMD   TEM 3   B14   GMD   TEM 3   B15   T122   TEM 3   B16   T124   TEM 3   B16   T125   TEM 3   B16   T125   TEM 3   B16   T125   TEM 3   B17   TEM 3   B18   T134   TEM 3   B18   T134   TEM 3   B19   T134   TEM 3   TE	NC	\$0			PI	ВІ	NC NC		
R + 4 -   A + 2   B + 4   T + 0 - 1   T E M   3		HOST		$\vdash_{\wedge}$	BACKPLANE	B2			
SND	R x 4+		A20	Н—		В3	T x 0 +		
R R S S S S S S S S S S S S S S S S S S	R x 4 -		A21	₩		B4	Tx0-	ITEM 3	
R R S S S S S S S S S S S S S S S S S S	GND		A22	1 <del>.X</del> —		B5	GND	ITEM 3	
SR   SR   SR   SR   SR   SR   SR   SR	R x 5+		A23	ш.		B6	TxI+	ITEM 3	
S876   A26   R27   B10	R×5-		A24	14.		B7	TxI-	ITEM 3	
SB48	GND		A25	Ιν_		B8	GND	ITEM 3	
\$838   A28   B9	SB7B		A26	1			NC		NOTE 3
\$888   A28   B11   GND   ITEM 3   \$889   A29   B12   REFCLK   ITEM 3   \$890   A30   B13   REFCLK   ITEM 3   \$810   A31   B14   GND   ITEM 3   \$815   T12   ITEM 3   \$816   T12   ITEM 3   \$816   T12   ITEM 3   \$817   GND   ITEM 3   \$816   T12   ITEM 3   \$817   GND   ITEM 3   \$818   T13   ITEM 3   \$819   T13   ITEM 3   \$810   T14   T15   \$820   GND   ITEM 3   \$820   GND   ITEM 3   \$821   NC   \$821   NC   \$821   NC   \$821   NC   \$821   NC   \$822   GND   ITEM 3   \$823   T14   T15   T15   \$824   A4   R10   ITEM 3   \$825   GND   ITEM 3   \$826   GND   ITEM 3   \$827   A4   R10   ITEM 3   \$828   GND   ITEM 3   \$829   A5   GND   ITEM 3   \$844   R10   ITEM 3   \$855   GND   ITEM 3   \$850   GND   ITEM 3   \$85	SB4B		A27	├—		BIO	CWAKE#	ITEM 4	NOTE I
SSB+   A29   B12   REFCLK+   ITEM 3	SB3B		A28	├─		B9	CBL_ID	ITEM 4	NOTE I
SSB-   A30	SB3B		A28	1		BII	GND	ITEM 3	
Section   Sect	SBB+		A29	144		B12	REFCLK+	ITEM 3	
R + 6 +   A + 2	SBB-		A30	₩—		B13	REFCLK-	ITEM 3	
R16	GND		A31	1×-		B14	GND	ITEM 3	
SHO	Rx6+		A32	14		B15	Tx2+	ITEM 3	
R + 7 +	R x 6 -		A33	14.		B16	Tx2-	ITEM 3	
R + 7	GND		A34	1X-		B17	GND	ITEM 3	
B20   GND   ITEM 3	R x 7+		A35	ш.		B18	Tx3+	ITEM 3	
Second   S	R x 7 -		A36	14-		B19	Tx3-	ITEM 3	
NC   B19	GND		A37	Ιν_		B20	GND		
A2   GND   ITEM 3	NC			1		B21	NC		
T144     B20     A3   R104   ITEM 3     A4   R105   ITEM 3     A5   GHD   ITEM 3     A5   GHD   ITEM 3     A5   GHD   ITEM 3     A5   GHD   ITEM 3     A7   R114   ITEM 3   A7   A7   A7   A7   A7   A7   A7	NC			1		Al	NC		
T14-	GND		B19	-		A2	GND	ITEM 3	
AS   GND   ITEM 3	Tx4+		B20	144		A3	R x 0+	ITEM 3	
T15+   B23   A6   R11+   1TEM 3     T15-   B24   A7   Rx1-   1TEM 3     B25   A8   GND   ITEM 3     B808   B26   A9   SCL   ITEM 4   NOTE     S818   B27   A10   SDA   ITEM 4   NOTE     S828   B28   B29   A11   GND   ITEM 3     S858   B29   A12   PERST#   ITEM 3     GND   B31   A14   GND   ITEM 3     T16+   B32   A15   Rx2-   ITEM 3     T16-   B33   A16   Rx2-   ITEM 3     GND   B34   A17   GND   ITEM 3     A18   Rx3+   ITEM 3     A18   Rx3+   ITEM 3     A18   Rx3+   ITEM 3     A19   Rx3-   ITEM 3     A19   Rx3-   ITEM 3     A19   Rx3-   ITEM 3     A20   GND   ITEM 3     A30   A30   A30   A30     A30   A30   A30   A30     A30   A30   A30   A30     A30   A30   A30   A30     A30   A30   A30     A30   A30   A30     A30   A30   A30     A30   A30   A30     A30   A30   A30     A30   A30   A30     A30   A30   A30     A30   A30   A30     A30   A30   A30     A30   A30   A30     A30   A30   A30     A30   A30   A30     A30   A30   A30     A30   A30   A30     A30   A30     A30   A30     A30   A30     A30   A30     A30   A30     A30   A30     A30   A30     A30   A30     A30   A30     A30   A30     A30   A30     A30   A30     A30   A30     A30   A30     A30   A30     A30   A30     A30   A30     A30   A30     A30	T x 4 -		B21	1		A4	R x 0 -	ITEM 3	
T15-	GND		B22	1X.		A5	GND	ITEM 3	
SB0B   B25   SB0B   B26   A8   GND   ITEM 3	T x 5+		B23	144—		A6	RxI+	ITEM 3	
SB0B   B25   A8	Tx5-		B24	ш		A7	RxI-	ITEM 3	
SB18   B27   SB28   B28   B28   A10   SDA   ITEM 4   NOTE   SB28   B28   A12   PERST#   ITEM 3   A12   PERST#   ITEM 3   A14   GND   ITEM 3   A14   GND   ITEM 3   A15   RX2+   ITEM 3   A15   RX2+   ITEM 3   A15   RX2+   ITEM 3   A16   Rx2   ITEM 3   A17   GND   ITEM 3   A18   Rx3+   ITEM 3   A18   Rx3+   ITEM 3   A18   Rx3+   ITEM 3   A18   Rx3+   ITEM 3   A19   Rx3-   ITEM 3   A20   GND   A20	GND		B25	$\vdash$		A8	GND	ITEM 3	
SB2B   B2B   B2B   A11   GND   1TEM 3	SBOB		B26	├—		A9	SCL	ITEM 4	NOTE I
S858   B29   A12   PERST®   ITEM 3	SBIB		B27	├		A10	SDA	ITEM 4	NOTE I
SB6B   B30   A13   D_INPLE   ITEM 3	SB2B		B28	$\vdash$		All			
GND   B31   A14   GND   ITEM 3	SB5B		B29	Н—		A12	PERST#	ITEM 3	
Tx6+   B32   A15   RX2+   ITEM 3	SB6B		B30	₩		A13	D_INPL#	ITEM 3	
T 16- B 33 GND B 34 T 17- B 35 A 16 R 12- I I EM 3 A 17 GND I I EM 3 A 18 R 13- I I EM 3 A 18 R 13- I I EM 3 A 19 R 23- I I EM 3 A 20 GND I I EM 3 A 20 GND I I EM 3	GND		B31	1X-		A14	GND	ITEM 3	
GND   B34   A17   GND   ITEM 3   Tx7+   B35   A18   Rx3+   ITEM 3   A19   Rx3-   ITEM 3   A20   GND	Tx6+		B32	Н—		A15	RX2+		
Tx7+ B35 A18 Rx3+ ITEM 3 Tx7- B36 A20 GND ITEM 3 A20 GND ITEM 3				1					
Tx7- B36 A19 Rx3- ITEM 3 A20 GND ITEM 3				1 <del>.X</del> —		A17	GND		
GND B37 A20 GND ITEM 3			B35	Н-		A18	Rx3+	ITEM 3	
	T x 7 -		B36	14		A19	Rx3-	ITEM 3	
10 10	GND		B37	$\vdash$		A20	GND	ITEM 3	
NC   AZI NC	NC					A21	NC		

### NOTES:

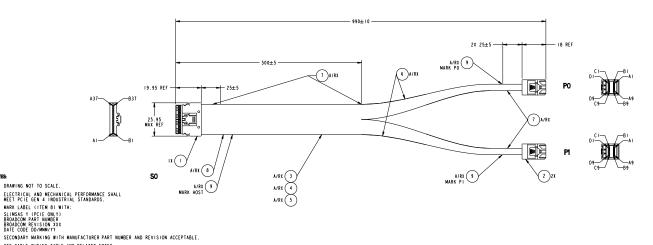
- I. END TO END RESISTANCE OF INDICATED CONNECTION SHALL BE 750 MILLIOHM MAX.
- 2. SHORT PIN/PAD SO-A8 TO SO-B9. NO DIRECT CONNECT SO-A8 TO CONNECTOR PO.
- 3. SHORT PIN/PAD SO-A26 TO SO-B27. NO DIRECT CONNECT SO-A26 TO CONNECTOR PI.

## Cable 05-60002-00

The following figure shows the drawing and pinout for Broadcom cable 05-60002-00, a x8 SFF-8654 to 2 x4 SFF-8643 connection. Use this cable for NVMe connections on SuperMicro Purley backplanes.

Figure 16: Cable 05-60002-00 Drawing and Pinout





4. 31	E CABLE WIR	ING TABLE AND RELATED NOTES.		
9	LABEL	LABEL, 40XI4MM, WHITE, R2, HF	A/R	
8	LABEL	LABEL, 70x26MM, WHITE, R2, HF	A/R	SEE NOTE :
7	TAPE	ACETATE TAPE; W=1 INCH	A/R	
6	SLEEVING	EXPANDO TUBE: OD:7MM, GREEN, VW-I, HF	A/R	
5	SLEEVING	EXPANDO TUBE: OD=10MM, GREEN, VW-1, HF	A/R	
4	WIRE	UL1061, STRANDED	A/R	SEE NOTE
3	CABLE	SAS CABLE: UL20744, 28-32AWG, 92.5Ω ±10% DIFF, PCIE GEN 4, VW-1, NATURAL, HF	A/R	
2	CONNECTOR	SFF-8643, 36P, STRAIGHT, X4, WHITE, SHORT, PCIE GEN 4	2	SEE NOTE
1	CONNECTOR	SFF-8654, 74P, STRAIGHT, X8, SAS 4.0, PCIE GEN 4	1	SEE NOTE
ITEM	CALLOUT	DESCRIPTION	QUANTITY	NOTES

APPROVALS	DATE	UNLESS OTHERWISE :	SPECIFIED:			7000	л•
COLUMN SY	TOLERANCES: X.X ± 0.2 MM		BROADCOM*				
APPROVED BY	X.XX ± 0.15 M X.XXX ± 0.050		TITLE	2 X			
Company Confidential © Copyright All Rights Reserved. Any copy is an	ANGLES ± 1°			SFF-8643	X4 (W), I	М	
The physiciser is responsible for a document's revision is current, passessor is responsible for re- documents from their poin	Merester, the Morise obsolete	1 . 500	⊕€	SIM D	5067-6862	003	SHEET

CONNECTOR SIGNAL	CONNECTOR (HOST)	PIN/PAD		CONNECTOR (TARGET)	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE	CONNECTOR SIGNAL	CONNECTOR (HOST)	PIN/PAD		CONNECTOR (TARGET)	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE
GND	\$0	Al	_	P0	D3	GND	ITEM 3	GND	S0	A19	Λ	PI	D3	GND	ITEM 3
R x 0 +	1	A2	144		D4	Tx0+	ITEM 3	R x 4+	1	A20			D4	Tx0+	ITEM 3
R×0-	1	A3	₩	-	D5	Tx0-	ITEM 3	R x 4 -	1	A21	++-		D5	Tx0-	ITEM 3
GND	1	A4	1 <del>.X</del> -		C3/D6	GND	ITEM 3, NOTE 2	GND	1	A22	X		C3/D6	GND	ITEM 3, NOTE 2
RxI+	1	A5	144	-	C4	TxI+	ITEM 3	Rx5+	1	A23			C4	Tx1+	ITEM 3
RxI-	1	A6	₩	-	C5	Tr1-	ITEM 3	Rx5-	1	A24	++-	-	C5	Tx1-	ITEM 3
GND	1	A7	μ_		C6	GND	ITEM 3	GND	1	A25	V		C6	GND	ITEM 3
SB7A	1	A8	1			NC	NOTE 3	SB7B	1	A26				NC	NOTE 5
SB4A		A9	1			NC		SB4B	1	A27				NC	
SB3A		AIO	-	-	A3	GND	ITEM 3	SB3B		A28	Λ	1	A3	GND	ITEM 3
SBA+	1	All	₩.	-	A2	REFCLK+	ITEM 3	SBB+	1	A29			A2	REFCLK+	ITEM 3
SBA-	1	A12	₩	-	Al	REFCLK-	ITEM 3	SBB-	1	A30	++-	1	Al	REFCLK-	ITEM 3
GND	1	A13	<del>  X -</del>	-	D6	GND	ITEM 3	GND	]	A31	HX-		D6	GND	ITEM 3
Rx2+		A14	₩-	-	D7	Tx2+	ITEM 3	Rx6+		A32	-	1	D7	Tx2+	ITEM 3
R x 2 -		A15	₩	1	D8	Tx2·	ITEM 3	R×6-		A33	++-	1	D8	Tx2-	ITEM 3
GND		A16	ŀX−	1	D9/C6	GND	ITEM 3, NOTE 2	GND		A34	X	1	D9/C6	GND	ITEM 3, NOTE 2
Rx3+		A17	₩-	-	C7	Tx3+	ITEM 3	Rx7+		A35	-	1	C1	Tx3+	ITEM 3
Rx3-		A18	₩	1	C8	Tx3-	ITEM 3	R x 7 -		A36	++-	1	C8	Tx3-	ITEM 3
GND	1	A19	μ_	-	C9	GND	ITEM 3	GND	1	A37	V	1	C 9	GND	ITEM 3
GND		ВІ	-	1	B3	GND	ITEM 3	GND		B19	Λ	1	B3	GND	ITEM 3
Tx0+	1	B2	144	-	B4	Rx0+	ITEM 3	Tx4+	1	B20			B4	Rx0+	ITEM 3
Tx0-		B3	₩	-	B5	R x 0 -	ITEM 3	Tx4-	1	B21	++-	1	B5	Rx0-	ITEM 3
GND	1	B4	1 <del>.X</del> —	-	B6/A3	GND	ITEM 3, NOTE 2	GND	1	B22	X		B6/A3	GND	ITEM 3, NOTE 2
TxI+	1	B5	144	-	A4	RxI+	ITEM 3	Tx5+	1	B23			A4	Rx I+	ITEM 3
Tx1-		B6	₩	-	A5	RxI-	ITEM 3	Tx5-	1	B24	++-	1	A5	RxI-	ITEM 3
GND		87	μ—	-	A6	GND	ITEM 3	GND		B25	V	1	A6	GND	ITEM 3
SBOA	1	88	├	-	DI	2W_CLK	ITEM 4, NOTE I	SBOB	1	B26			DI	2W_CLK	ITEM 4, NOTE I
SBIA		B9	├	-	D2	2W_SDA	ITEM 4, NOTE I	SBIB	1	B27	_	1	D2	2W_SDA	ITEM 4, NOTE I
SB2A		810	]			NC	SHORT TO GND	SB2B	]	B28				NC	SHORT TO GND
SB5A	1	BII	$\vdash$	-	B2	PERST#	ITEM 4, NOTE I	SB5B	1	B29			B2	PERST#	ITEM 4, NOTE I
SB6A	1	B12	1			NC	SHORT TO GND	SB6B	1	B30				NC	SHORT TO GND
GND	1	B13	-	-	B6	GND	ITEM 3	GND	]	B31	Λ	1	B6	GND	ITEM 3
Tx2+	1	B14	ш.	-	87	RX2+	ITEM 3	Tx6+	1	B32			B7	RX2+	ITEM 3
Tx2-	1	B15	₩	-	B8	Rx2-	ITEM 3	Tx6-	1	B33	++-		B8	Rx2-	ITEM 3
GND	1	B16	1 <del>.X</del> —	-	B9/A6	GND	ITEM 3, NOTE 2	GND	1	B34	X		B9/A6	GND	ITEM 3, NOTE 2
Tx3+		817	₩-	-	A7	Rx3+	ITEM 3	Tx7+	]	B35	H -		A7	Rx3+	ITEM 3
Tx3-	1	B18	₩	-	A8	Rx3-	ITEM 3	Tx7-	1	B36	+-		A8	Rx3-	ITEM 3
GND	1	B19	μ_	-	A9	GND	ITEM 3	GND	1	B37	V		A 9	GND	ITEM 3
	1		1		CI	NC			1				CI	NC	
	1		1		C2	NC			1				C2	NC	
	1		1		BI	NC.			1			1	BI	NC NC	

- TESE
  END TO END RESISTANCE OF INDICATED CONNECTION SHALL BE 750 MILLIOHM MAX.
  CONNECT SHIELD OF DIFFERENTIAL PAIR TO INDICATED PIN/PAD AND SHORT TO SECOND INDICATED PIN/PAD.
  SHORT PIN/PAD S0-AB TO S0-BB. NO DIRECT CONNECT S0-AB TO CONNECTOR PD.
  DELETED
  SHORT PIN/PAD S0-A26 TO S0-B27. NO DIRECT CONNECT S0-A26 TO CONNECTOR PI.
  DELETED

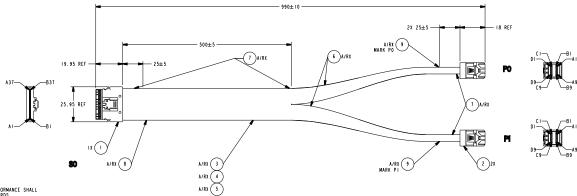
96xx-MR-HBA-Tri-Mode-UG109 Broadcom

## Cable 05-60003-00

The following figure shows the drawing and pinout for Broadcom cable 05-60003-00, a x8 SFF-8654 to 2 x4 SFF-8643 connection.

Figure 17: Cable 05-60003-00 Drawing and Pinout

	REVISIONS										
REV	ECO	DESCRIPTION	DATE	APPROVED							
001		PRELIMINARY RELEASE	28FEB18								
002	175141	UPDATE WIRE GAUGE AND IMPEDANCE TOLERANCE ITEM 3. REVISE MARKING REQUIREMENTS NOTE 3.	21NOV19								



- DAANING NOT 10 SCALE. ELECTRICAL AND MECHANICAL PERFORMANCE SHALL MEET SAS 4.0 INDUSTRIAL STANDARDS.

  MARK LABEL (ITEM 8) WITHS SLIMSAS Y (SFF-9402 SAS)
  BROADCOM PART NUMBER BROADCOM REVISION XXX

  DATE COME DOWNMANTY

DRAWING NOT TO SCALE.

- SECONDARY MARKING WITH MANUFACTURER PART NUMBER AND REVISION ACCEPTABLE.
- 4. SEE CABLE WIRING TABLE AND RELATED NOTES.

9	LABEL	LABEL, 40XI4MM, WHITE, R2, HF	A/R	
8	LABEL	LABEL, 70X26MM, WHITE, R2, HF	A/R	SEE NOTE 3
7	TAPE	ACETATE TAPE: W=1 INCH	A/R	
6	SLEEVING	EXPANDO TUBE: OD=7MM, BLACK, VW-I, HF	A/R	
5	SLEEVING	EXPANDO TUBE: OD:IOMM, BLACK, VW-I, HF	A/R	
4	WIRE	UL1061, STRANDED	A/R	SEE NOTE 4
3	CABLE	SAS CABLE: UL20744, 28-32AWG, 92.5Ω ±10% DIFF, PCIE GEN 4, VW-1, NATURAL, HF	A/R	
2	CONNECTOR	SFF-8643, 36P, STRAIGHT, X4, BLACK, SHORT, SAS 4.0	2	SEE NOTE 4
- 1	CONNECTOR	SFF-8654, 74P, STRAIGHT, X8, SAS 4.0, PCIE GEN 4	i	SEE NOTE 4
ITEM	CALLOUT	DESCRIPTION	QUANTITY	NOTES

APPROVALS	DATE ISFEBI8	UNLESS OTHERWISE DIMENSIONS ARE II			A) BRO	ADCOR	<b>1</b> •		
APPROVED BY	28FEB18	X,X ± 0.2 MN X.XX ± 0.15 X.XXX ± 0.05	MN	TITLE	" CABLE, SFF-8654 X8 TO 2X				
Company Confidential & Copyright All Rights Reserved, Any copy is an The possessor is responsible for document's revision is current possessor is responsible for re	uscantrolled copy, verifying that the . Moreover, the moving absolete	scale 1,500	⊕€1	sin D	SFF-8 006 NO. 5067-6866	643 X4, IM 847 002	SHEET		

CONNECTOR SIGNAL	CONNECTOR	PIN/PAD		CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE	NOTES
GND	S0	Al	^	P0	D3	GND	ITEM 3	
Rx0+	1	A2	1/ 1		D4	Tx0+	ITEM 3	
Rx0-	1	A3	111	-	D5	Tx0-	ITEM 3	
GND	1	A4	1-X-		C3/D6	GND	ITEM 3	NOTE 2
RxI+	1	A5	1//		C4	Tx1+	ITEM 3	
RxI-	1	A6	1	-	C5	Tx1-	ITEM 3	
GND	1	A7	1ν_	-	C6	GND	ITEM 3	
SB7A	1	A8	1—	-	A2	SB7	ITEM 4	NOTE I
SB4A	1	A9	Ъ—		C2	SB4	ITEM 4	NOTE I
SB3A	1	AIO	1—		B2	SB3	ITEM 4	NOTE I
SBA+	1	All	1			NC		
SBA-	1	A12	1			NC		
GND	1	A13	-	-	D6	GND	ITEM 3	
Rx2+	1	A14	₩		D7	Tx2+	ITEM 3	
Rx2-	1	A15	14.		D8	Tx2-	ITEM 3	
GND	1	A16	1 <del>.X</del> —	-	D9/C6	GND	ITEM 3	NOTE 2
Rx3+	1	A17	144	-	C 7	Tx3+	ITEM 3	
Rx3-	1	818	₩	-	C8	Tx3-	ITEM 3	
GND	]	A19	$\vdash$	-	C 9	GND	ITEM 3	
GND	1	ВІ	1	-	В3	GND	ITEM 3	
Tx0+	1	B2	₩-	-	B4	Rx0+	ITEM 3	
Tx0-	]	B3	14/	-	B5	Rx0-	ITEM 3	
GND	1	84	1 <del>.X</del> –	-	B6/A3	GND	ITEM 3	NOTE 2
Tx1+	]	B5	144	-	A4	RxI+	ITEM 3	
Tx1-	]	B6	₩	-	A5	RxI-	ITEM 3	
GND		B7	$\vdash$	1	A6	GND	ITEM 3	
SBOA	]	B8	$\vdash$	-	AI	SB0	ITEM 4	NOTE I
SBIA		B9	$\vdash$	-	ВІ	\$BI	ITEM 4	NOTE I
SB2A	]	BIO	-	-	CI	SB2	ITEM 4	NOTE I
SB5A	]	BII	$\vdash$	-	D2	SB5	ITEM 4	NOTE I
SB6A		B12	-	1	DI	SB6	ITEM 4	NOTE I
GND	]	B13		1	B6	GND	ITEM 3	
Tx2+		B14	н-	1	B7	RX2+	ITEM 3	
Tx2-		B15	н —	1	B8	Rx2-	ITEM 3	
GND	]	B16	H-	1	B9/A6	GND	ITEM 3	NOTE 2
Tx3+		B17	Н-	1	A7	Rx3+	ITEM 3	
Tx3-	]	B18	14	1	A8	Rx3-	ITEM 3	
GND		B19	$\vdash^{\vee}$		A9	GND	ITEM 3	

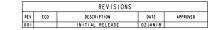
CONNECTOR SIGNAL	CONNECTOR	PIN/PAD		CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE	NOTES
GND	S0	A19	_	PI	D3	GND	ITEM 3	
Rx4+	1	A20	144		D4	Tx0+	ITEM 3	
Rx4-	1	A21	1		D5	Tx0-	ITEM 3	
GND	1	A22	1.X.		C3/D6	GND	ITEM 3	NOTE 2
Rx5+	1	A23	144		C4	TxI+	ITEM 3	
Rx5-	1	A24	1		C5	Tx1-	ITEM 3	
GND	1	A25	1ν		C6	GND	ITEM 3	
SB7B	1	A26	1—		A2	\$87	ITEM 4	NOTE I
SB4B	1	A27	<u> </u>		C2	SB4	ITEM 4	NOTE I
SB3B	1	A28	1—		B2	SB3	ITEM 4	NOTE I
SBB+	1	A29	1			NC		
SBB-	1	A30	1			NC		
GND	1	A31	<del> </del>		D6	GND	ITEM 3	
Rx6+	1	A32	144		D7	Tx2+	ITEM 3	
Rx6-	1	A33	₩		D8	Tx2-	ITEM 3	
GND	1	A34	1-X-		D9/C6	GND	ITEM 3	NOTE 2
Rx7+	1	A35	144		C7	T x 3+	ITEM 3	
Rx7-	1	A36	1		C8	Tx3-	ITEM 3	
GND	1	A37	14		C 9	GND	ITEM 3	
GND	1	B19	<del> </del>		В3	GND	ITEM 3	
T x 4+	1	B20	144		B4	Rx0+	ITEM 3	
T x 4 -	1	B21	₩.		B5	R×0-	ITEM 3	
GND	1	B22	1-X-		B6/A3	GND	ITEM 3	NOTE 2
Tx5+	1	B23	144		A4	RxI+	ITEM 3	
Tx5-	1	B24	111		A5	RxI-	ITEM 3	
GND	1	B25	$\sim$		A6	GND	ITEM 3	
SBOB	1	B26	├—		Al	SB0	ITEM 4	NOTE I
SBIB	1	B27	├—		ВІ	SBI	ITEM 4	NOTE I
SB2B	1	B28	$\vdash$		CI	SB2	ITEM 4	NOTE I
SB5B	1	B29	├─		D2	SB5	ITEM 4	NOTE I
SB6B	1	B30	├—		DI	SB6	ITEM 4	NOTE I
GND	1	B31	-		B6	GND	ITEM 3	
Tx6+	1	B32	144		87	RX2+	ITEM 3	
Tx6-	1	B33	₩		B8	Rx2-	ITEM 3	
GND	1	B34	1 <del>.X</del> —		B9/A6	GND	ITEM 3	NOTE 2
T x 7+	1	B35	144—		A7	Rx3+	ITEM 3	
T x 7 -	1	B36	14		A8	Rx3-	ITEM 3	
GND	1	B37	μ_		A9	GND	ITEM 3	

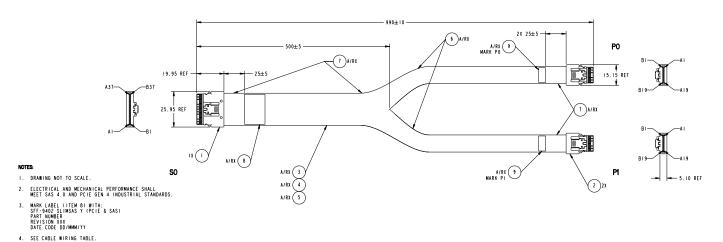
I. END TO END RESISTANCE OF INDICATED CONNECTION SHALL BE 750 MILLIOHM MAX.
2. CONNECT SHIELD OF DIFFERENTIAL PAIR TO INDICATED PIN/PAD AND SHORT TO SECOND INDICATED PIN/PAD.

## Cable 05-60004-00

The following figure shows the drawing and pinout for Broadcom cable 05-60004-00, a x8 SFF-8654 to 2 x4 SFF-8654 connection.

Figure 18: Cable 05-60004-00 Drawing and Pinout





	9	LABEL	LABEL, 40XI4MM, WHITE, R2, HF	A/R	
Г	8	LABEL	LABEL, TOX26MM, WHITE, R2, HF	A/R	SEE NOTE 3
	7	TAPE	ACETATE TAPE: W=1 INCH	A/R	
Γ	6	SLEEVING	EXPANDO TUBE: OD=7MM, GREEN, VW-I, HF	A/R	
Г	5	SLEEVING	EXPANDO TUBE: OD:IOMM, GREEN, VW-I, HF	A/R	
Г	4	WIRE	ULLOGI. 32AWG. STRANDED	A/R	

- 1		LADEL	LADLE, TOXEDAR, WITTE, NZ, III	A / IV	JEL MOIL J
	7	TAPE	ACETATE TAPE: W=1 INCH	A/R	
	6	SLEEVING	EXPANDO TUBE: OD:7MM, GREEN, VW-I, HF	A/R	
	5	SLEEVING	EXPANDO TUBE: OD=10MM, GREEN, VW-1, HF	A/R	
	4	WIRE	UL1061, 32AWG, STRANDED	A/R	
	3	CABLE	SAS CABLE: UL20744, 32AWG, 85OHM DIFF, SAS4, PCIE GEN 4, VW-I, NATURAL, HF	A/R	
	2	CONNECTOR	SFF-8654, 38P, STRAIGHT, X4, SAS 4.0, PCIE GEN 4	2	SEE NOTE 4
	1	CONNECTOR	SFF-8654, 74P, STRAIGHT, X8, SAS 4.0, PCIE GEN 4	1	SEE NOTE 4
T	TEM	CALLOUT	DESCRIPTION	QUANTITY	NOTES

APPROVALS SPARE BY	DATE 27DEC17	UNLESS OTHERWISE DIMENSIONS ARE IN TOLERANCES: X.X ± 0.2 MM	SPECIFIED: MILLIMETERS.	♠ BROADCOM¹				
APPROVED BY 02 JAN 18 Company Confidential O Copyright Broadcon Limited All Rights Reserved Any Copyright Broadcon Limited		X.XX ± 0.15 N X.XXX ± 0.050 ANGLES ± 1	N HM	CABLE, SFF-8654 X8 TO 2X SFF-8654 X4, IM				
The possesser is responsible for a document's revision is current, possesser is responsible for re documents from their poin	1.500	<b>⊕</b> €	SIZE D	5067-6103	001	SHEET		

GND   Rx0+   Rx0-   GND   Rx1+   Rx1-   GND   SB7A   SB4A   GND(SB3A)	SO	A1 A2 A3 A4 A5 A6		PO	B1 B2 B3 B4	GND Tx0+ Tx0-	ITEM 3 ITEM 3 ITEM 3
Rx0- GND Rx1+ Rx1- GND SB7A SB4A GND(SB3A)		A3 A4 A5 A6			B3	Tx0-	
GND RxI+ RxI- GND SB7A SB4A GND(SB3A)		A4 A5 A6					ITEM 3
RxI+ RxI- GND SB7A SB4A GND(SB3A)		A5 A6			B4		
RxI- GND SB7A SB4A GND(SB3A)		A6				GND	ITEM 3
GND SB7A SB4A GND(SB3A)			ш.		B5	TxI+	ITEM 3
SB7A SB4A GND(SB3A)		A7	$\Pi$		B6	TxI-	ITEM 3
SB4A GND(SB3A)			$\vdash$		87	GND	ITEM 3
GND(SB3A)		A8	$\vdash$		B8	SB7	ITEM 4
		A 9	$\vdash$		B9	SB4	ITEM 4
		A10	1		B10	GND(SB3)	ITEM 3
SBA+		ALL	₩—		BII	SBA+	ITEM 3
SBA-		A12	11.		B12	SBA-	ITEM 3
GND		A13	1-X-		B13	GND	ITEM 3
Rx2+		A14	₩-		B14	Tx2+	ITEM 3
Rx2-		A15	11 /		B15	T x 2 -	ITEM 3
GND		A16	1 <del>.X</del> —		B16	GND	ITEM 3
Rx3+		A17	144		B17	Tx3+	ITEM 3
Rx3-		AI8	11		B18	Tx3-	ITEM 3
GND		A19	$\sim$		B19	GND	ITEM 3, NOTE I
GND		BI	1		AI	GND	ITEM 3
Tx0+		B2	1//		A2	R x 0 +	ITEM 3
Tx0-		B3	₩—		A3	RxO-	ITEM 3
GND		B4	1-X-		A4	GND	ITEM 3
Tx I +		B5	₩-		A5	RxI+	ITEM 3
Tx1-		B6	11/		A6	RxI-	ITEM 3
GND		B7	1ν_		A7	GND	ITEM 3
SBOA		B8	1—		A8	SB0	ITEM 4
SBIA		B9	1—		A9	SBI	ITEM 4
GND(SB2A)		BIO	1_		AIO	GND(SB2)	ITEM 3
SB5A		BII	1//		ALI	SB5	ITEM 3
SB6A		B12	14		A12	SB6	ITEM 3
GND		B13	1.X.		A13	GND	ITEM 3
Tx2+		B14	144-		A14	RX2+	ITEM 3
Tx2-		B15	₩-		A15	Rx2-	ITEM 3
GND		B16	1-X-		A16	GND	ITEM 3
Tx3+		B17	144		A17	Rx3+	ITEM 3
Tx3-		B18	14		A18	Rx3-	ITEM 3
GND		B19	μ_		A19	GND	ITEM 3, NOTE I

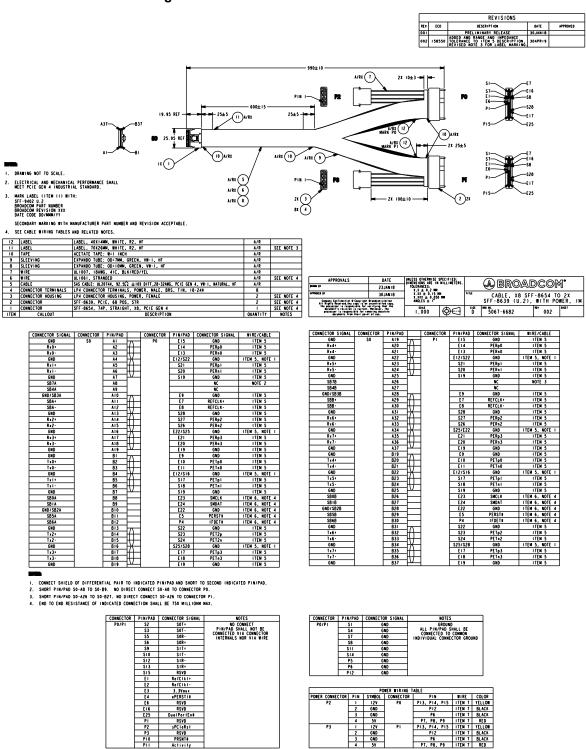
CONNECTOR SIGNAL	CONNECTOR	PIN/PAD		CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE
GND	SO SO	A19	$\overline{}$	PI	BI	GND	ITEM 3, NOTE I
Rx4+		A20	н-		B2	T x 0+	ITEM 3
R x 4 -		A21	11/	-	B3	T x 0 -	ITEM 3
GND		A22	<b>⊢</b> X−		B4	GND	ITEM 3
Rx5+		A23	Η —	-	B5	TxI+	ITEM 3
R x 5 -		A24	H <i>I</i> –	-	B6	TxI-	ITEM 3
GND		A25	$\vdash$		87	GND	ITEM 3
SB7B		A26	-	-	B8	SB7	ITEM 4
SB4B		A27	$\vdash$		B9	SB4	ITEM 4
GND(SB3B)		A28	-	-	BIO	GND(SB3)	ITEM 3
SBB+		A29	н –		BII	SBA+	ITEM 3
SBB -		A30	14/	-	812	SBA-	ITEM 3
GND		A31	1 <del>.X</del> –	-	B13	GND	ITEM 3
Rx6+		A32	ж	-	B14	Tx2+	ITEM 3
Rx6-		A33	₩	-	B15	Tx2-	ITEM 3
GND		A34	1 <del>.X</del> —		B16	GND	ITEM 3
Rx7+	1	A35	144-		817	Tx3+	ITEM 3
Rx7-	1	A36	14		B18	Tx3-	ITEM 3
GND		A37	$\vdash$		BI9	GND	ITEM 3
GND	1	B19	1		AI	GND	ITEM 3, NOTE I
Tx4+	1	B20	144—		A2	R x 0 +	ITEM 3
T x 4 -	1	B21	1111		A3	R x 0 -	ITEM 3
GND	1	B22	1-X-		A4	GND	ITEM 3
Tx5+	1	B23	₩-		A5	RxI+	ITEM 3
Tx5-	1	B24	11.		A6	RxI-	ITEM 3
GND	1	B25	1V.		A7	GND	ITEM 3
SBOB	1	B26	1		A8	SB0	ITEM 4
SBIB	1	B27	1—		A 9	SBI	ITEM 4
GND(SB2B)	1	B28	1_		A10	GND(SB2)	ITEM 3
SB5B	1	B29	144-		AII	SB5	ITEM 3
SB6B	1	B30	14		A12	SB6	ITEM 3
GND	i	B31	1X—		A13	GND	ITEM 3
Tx6+	1	B32	144-		A14	RX2+	ITEM 3
Tx6-	1	B33	₩		A15	Rx2-	ITEM 3
GND	1	B34	1.X.		A16	GND	ITEM 3
Tx7+	1	B35	1//		A17	Rx3+	ITEM 3
T x 7 -		B36	₩_		A18	Rx3-	ITEM 3
GND	1	B37	1.V.	l	A19	GND	ITEM 3

NOTES:
I. PIN/PAD ON CONNECTOR SO SHARED ON CONNECTORS PO AND PI.

## Cable 05-60005-00

The following figure shows the drawing and pinout for Broadcom cable 05-60005-00, a x8 SFF-8654 to 2 U.2 SFF-8639 connection.

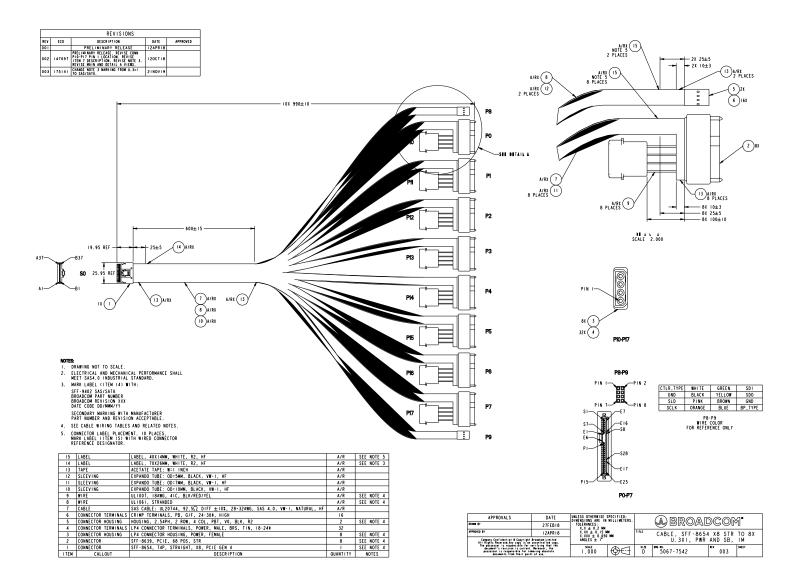
Figure 19: Cable 05-60005-00 Drawing and Pinout



## Cable 05-60006-00

The following figure shows the drawing and pinout for Broadcom cable 05-60006-00, a x8 SFF-8654 to x8 U.3 SFF-8639 connection.

Figure 20: Cable 05-60006-00 Drawing and Pinout



CONNECTOR SIGNAL	CONNECTOR	PIN/PAD		CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE	NOTES
GND	S0	Al	$\wedge$	P0	\$7	GND	ITEM 7	
R x 0 +	HOST	A2	1//	TARGET	S6	DO_TXO+	ITEM 7	
R x 0 -		A3	11 /		\$5	DO_TXO-	ITEM 7	
GND		A4	Ι		\$4	GND	ITEM 7	NOTE 2, NOTE 3
GND		BI	$\vdash_{\wedge}$		\$1	GND	ITEM 7	
T x 0 +		B2	H		\$2	D0_RX0+	ITEM 7	
T x 0 -		B3	H -		\$3	DO_RXO-	ITEM 7	
GND		B4	$\vdash$		\$4	GND	ITEM 7	NOTE 2, NOTE 3
127	PIO		_	P0	P13, P14, P15	127	ITEM 9	YELLOW
GND		2	<u> </u>	TARGET	P12	GND	ITEM 9	BLACK
GND		3			P5, P6	GND	ITEM 9	BLACK
5V		4			P7, P8, P9	5V	ITEM 9	RED
NC				P0	PI	RSVD		NOTE 4
NC			느느	TARGET	P2	s PC I e R s t		NOTE 4
CONNECTOR SIGNAL	CONNECTOR	PIN/PAD		CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE	NOTES
GND	S0	A4	_	PI	\$7	GND	ITEM 7	NOTE 2
Rx I +	HOST	A5	1//	TARGET	S 6	DI_TXO+	ITEM 7	
RxI-		A6	ш_		\$5	DI_TXO-	ITEM 7	
GND		A7	LV		S4	GND	ITEM 7	NOTE 3
GND		B4	_		S1	GND	ITEM 7	NOTE 2
Tx1+		B5	1/1		\$2	DI_RXO+	ITEM 7	
Tx1-		B6	ш_		\$3	DI_RXO-	ITEM 7	
GND	i	B7	$\mathcal{V}$		S4	GND	ITEM 7	NOTE 3
12V	PII	1		PI	PI3, PI4, PI5	127	ITEM 9	YELLOW
GND		2		TARGET	P12	GND	ITEM 9	BLACK
GND		3			P5, P6	GND	ITEM 9	BLACK
5V		4	<b>—</b>		P7, P8, P9	5V	ITEM 9	RED
NC				PI	PI	RSVD		NOTE 4
NC				TARGET	P2	sPC   eRs t		NOTE 4
CONNECTOR SIGNAL	CONNECTOR	PIN/PAD		CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE	NOTES
GND	SO	AI3		P2	S7	GND GND	ITEM 7	NOTES
Rx2+	HOST	AI4	Λ	TARGET	\$6	D2_TX0+	ITEM 7	
Rx2-		A14	П		S5	D2_TX0+	ITEM 7	
GND		A16	W		S4	GND	ITEM 7	NOTE 2, NOTE 3
GND		BI3			S1	GND	ITEM 7	NUIE Z, NUIE 3
Tx2+		B14	Π		\$2	D2_RX0+	ITEM 7	
Tx2-		B15			\$3	D2_RX0+	ITEM 7	
			17				ITEM 7	NOTE 2 NOTE 2
GND 12V	P12	B16		P2	\$4 PI3, PI4, PI5	GND 12V	ITEM 7	NOTE 2, NOTE 3 YELLOW
GND	FIZ	2		TARGET	P13, P14, P15	GND	ITEM 9	
GND		3			P5. P6	GND	ITEM 9	BLACK BLACK
5V		4			P7. P8. P9	5V	ITEM 9	RED
NC NC		-4-		P2	P1, P8, P9	RSVD	TIEM 9	NOTE 4
NC NC				TARGET	P2			
NC					r2	s PC TeRs t		NOTE 4

CONNECTOR SIGNAL	CONNECTOR	PIN/PAD		CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE	NOTES
GND	SO	A3I		P6	\$7	GND	ITEM 7	MOTES
Rx6+	ност	A32	Δ	TARGET	\$6	D6_TX0+	ITEM 7	
Rx6-	1	A33	Ш		\$5	D6_TXO-	ITEM 7	
GND	1	A34	V		\$4	GND	ITEM 7	NOTE 2, NOTE 3
GND	1	B31			S1	GND	ITEM 7	MOTE E, MOTE 5
Tx6+	1	B32	$\Delta$		\$2	D6_RX0+	ITEM 7	
Tx6-	1	B33	Ш		\$3	D6_RXO-	ITEM 7	
GND	1	B34	LV_		\$4	GND	ITEM 7	NOTE 2, NOTE 3
127	P16	1		P6	P13, P14, P15	127	ITEM 9	YELLOW
GND	1	2		TARGET	P12	GND	ITEM 9	BLACK
GND	1	3			P5, P6	GND	ITEM 9	BLACK
5V	1	4			P7, P8, P9	5V	ITEM 9	RED
NC				P6	PI	RSVD		NOTE 4
NC	1		1 L	TARGET	P2	sPC   eRs t		NOTE 4
CONNECTOR SIGNAL	CONNECTOR	PIN/PAD		CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE	NOTES
GND	S0	A34	$\Lambda$	P7	\$7	GND	ITEM 7	NOTE 2
R x 7+	HOST	A35	H	TARGET	\$6	D7_TX0+	ITEM 7	
R x 7 -	1	A36	11		\$5	D7_TXO-	ITEM 7	
GND	1	A37		1	\$4	GND	ITEM 7	NOTE 3
GND	1	B34	$\Lambda$		\$1	GND	ITEM 7	NOTE 2
T x 7+	1	B35	H		\$2	D7_RX0+	ITEM 7	
T x 7 -		B36	H		\$3	D7_RX0-	ITEM 7	
GND		B37	V		\$4	GND	ITEM 7	NOTE 3
127	P17			P7 TARGET	P13, P14, P15	127	ITEM 9	YELLOW
GND	1	2		IAKGEI	P12	GND	ITEM 9	BLACK
GND	1	3			P5, P6	GND	ITEM 9	BLACK
5V		4			P7, P8, P9	5V	ITEM 9	RED
NC	4			P 7 TARGET	PI	RSVD		NOTE 4
NC				TARGET	P2	sPC   eRs t		NOTE 4
CONNECTOR SIGNAL	CONNECTOR	PIN/PAD		CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE	NOTES
SB7A	S0	A8		P8	8	BP_TYPEA	ITEM 8	NOTE I, BLUE
SB4A	HOST	A 9	1	P8	4	SDOA	ITEM 8	NOTE I, YELLOW
GND/SB3A	1	A10	<b></b>	P8	6	GND	ITEM 8	NOTE I, BROWN
SBA+	1	AII	1			NC		
SBA-	1	A12	1			NC		
SBOA	1	B8		P8	7	SCLKA	ITEM 8	NOTE I, ORANGE
SBIA	1	B9	-	P8	5	SLDA	ITEM 8	NOTE I, PINK
GND/SB2A	1	BIO		P8	3	GND	ITEM 8	NOTE I, BLACK
SB5A	1	BII		P8	2	SDIA	ITEM 8	NOTE I, GREEN
SB6A	1	B12		P8	1	CTLR_TYPEA	ITEM 8	NOTE I, WHITE
SB7B	S0	A26		P9	8	BP_TYPEB	ITEM 8	NOTE I, BLUE
SB4B	HOST	A27		P9	4	SDOB	ITEM 8	NOTE I, YELLOW
GND/SB3B		A28	}	P9	6	GND	ITEM 8	NOTE I, BROWN
SBB+		A29	]			NC		
SBB -	]	A30	1			NC		
SBOB	]	B26	-	P9	7	SCLKB	ITEM 8	NOTE I, ORANGE
SBIB	]	B27	-	P9	5	SLDB	ITEM 8	NOTE I, PINK
GND/SB2B	]	B28		P9	3	GND	ITEM 8	NOTE I, BLACK
SB5B	]	B29	<del></del>	P9	2	SDIB	ITEM 8	NOTE I, GREEN
SB6B	1	B30	<b>—</b>	P9	1	CTLR_TYPEB	ITEM 8	NOTE I, WHITE

CONNECTOR SIGNAL	CONNECTOR	PIN/PAD		CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE	NOTES
GND	SO	A16		P3	\$7	GND	ITEM 7	NOTE 2
Rx3+	HÖST	A17	łΔ_	TARGET	\$6	D3_TX0+	ITEM 7	NOTE 2
Rx3-		A18	Ш		\$5	D3_TXO-	ITEM 7	
GND		A19	LV_		\$4	GND	ITEM 7	NOTE 2. NOTE 3
GND		B16			S I	GND	ITEM 7	NOTE 2
Tx3+		B17	Δ		\$2	D3_RX0+	ITEM 7	MOTE Z
Tx3-		B18	Ш		S3	D3_RXO-	ITEM 7	
GND		B19	LV.		\$4	GND	ITEM 7	NOTE 2, NOTE 3
120	P13	1		P3	PI3, PI4, PI5	120	ITEM 9	YELLOW
GND	113	2		TARGET	P12	GND	ITEM 9	BLACK
GND		3			P5. P6	GND	ITEM 9	BLACK
5V		4			P7. P8. P9	5V	ITEM 9	RED
NC NC		-		P3	PI PI	RSVD	IIIEM 3	NOTE 4
NC NC			1	TARGET	P2	sPC LeRs t		NOTE 4
NC NC					FZ.	SPCIENST		NOTE 4
CONNECTOR SIGNAL	CONNECTOR	PIN/PAD		CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE	NOTES
GND	S0	A19	_	P4	\$7	GND	ITEM 7	NOTE 2
R x 4+	HOST	A20	1//	TARGET	S 6	D4_TX0+	ITEM 7	
R x 4 -		A21	ш		\$5	D4_TXO-	ITEM 7	
GND		A22	ΙV		S4	GND	ITEM 7	NOTE 2, NOTE 3
GND		B19	<del>                    _       _     _</del>		SI	GND	ITEM 7	NOTE 2
Tx4+		B20	1//		\$2	D4_RX0+	ITEM 7	
Tx4-		821	1		\$3	D4_RX0-	ITEM 7	
GND		B22	<u> </u>		S.4	GND	ITEM 7	NOTE 2, NOTE 3
12V	P14	ı	=	P4	P13, P14, P15	127	ITEM 9	YELLOW
GND		2	ऻ	TARGET	P12	GND	ITEM 9	BLACK
GND		3	ऻ		P5. P6	GND	ITEM 9	BLACK
5V		4	ऻ		P7. P8. P9	5V	ITEM 9	RED
NC				P4	PI	RSVD		NOTE 4
NC				TARGET	P2	sPCleRst		NOTE 4
CONNECTOR SIGNAL	CONNECTOR	PIN/PAD		CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE	NOTES
GND	SO HOST	A22		P5 TARGET	\$7	GND	ITEM 7	NOTE 2
Rx5+	HOST	A23	Н	TARGET	S 6	D5_TX0+	ITEM 7	
R x 5 -		A24	Н/		\$5	D5_TXO-	ITEM 7	
GND		A25	$\vdash$		\$4	GND	ITEM 7	NOTE 3
GND		B22			\$1	GND	ITEM 7	NOTE 2
Tx5+		B23	Н		\$2	D5_RX0+	ITEM 7	
Tx5-		B24	Ш		\$3	D5_RXO-	ITEM 7	
GND		B25	$\vdash$		\$4	GND	ITEM 7	NOTE 3
12V	P15	- 1	<u> </u>	P5	P13, P14, P15	127	ITEM 9	YELLOW
GND		2	$\vdash$	TARGET	P12	GND	ITEM 9	BLACK
GND		3	$\vdash$		P5, P6	GND	ITEM 9	BLACK
5V		4	$\vdash$		P7, P8, P9	5V	ITEM 9	RED
NC				P5	PI	RSVD		NOTE 4
NC			L └	TARGET	P2	s PC Le R s t		NOTE 4

CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	NOTES
P0-P7	\$9	\$17+	NO CONNECT
TARGET	\$10	\$17-	PIN/PAD SHALL NOT BE CONNECTED VIA CONNECTOR
	\$12	SIR-	INTERNALS NOR VIA WIRE
	\$13	SIR+	
	\$17	S2T+	
	\$18	S2T-	
	\$20	S2R-	
	\$21	S2R+	
	\$23	\$3T+	
	\$24	S3T -	
	\$26	S3R-	
	\$27	S3R+	
	P3	PWRDIS	
	P4	IFDET#	
	PIO	PRSNT#	
	PII	Activity/DisableStaggeredSpinup	
	ΕI	RefClk+	
	E 2	RefClkl-	
	E3	3.3Vaux	
	E 4	ePERSTI#	
	E 5	ePERSTO#	
	E 6	IFDET2#	
	E 7	RefCIk0+	
	E8	RefCIk0-	
	E 9	GND	
	EIO	PETp0	
	EII	PETn0	
	E12	GND	
	E13	PERn0	
	E I 4	PERp0	
	E15	GND	
	E16	HPTI	
	E 1 7	PETp3	
	E18	PETn3	
	E19	GND	
l l	E 2 0	PERn3	
l l	E21	PERp3	
	E 2 2	GND	
	E23	SMC I k	
	E24	SMDat	
	E25	DualPortEn#	
PO-P7 TARGET	\$8	GND	GROUND ALL PIN/PAD SHALL BE
IMRULI	\$11	GND	CONNECTED TO COMMON
	\$14	GND	INDIVIDUAL CONNECTOR GROUND
	\$15	GND (HPTO)	
l l	\$16	GND	
l l	\$19	GND	
	\$22	GND	
	\$25	GND	
	\$28	GND	

- NOTES:

  I. END TO END RESISTANCE OF INDICATED CONNECTION SHALL BE 750 MILLIOHM MAX.

  2. SHARED PIN/PAD ON HOST (50) CONNECTOR.

  3. SHARED PIN/PAD PI TO P2 OF INDICATED TARGET (P0-P7) CONNECTOR.

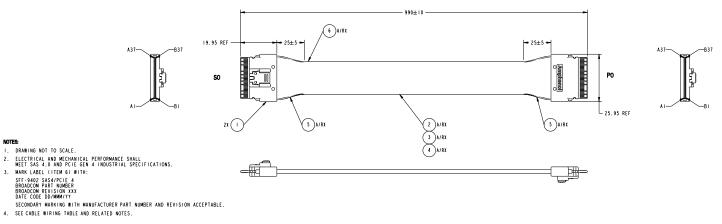
  4. SHORT PIN/PAD PI TO P2 OF INDICATED TARGET (P0-P7) CONNECTOR. NO CONNECT TO OTHER CONNECTORS.

## Cable 05-60007-00

The following figure shows the drawing and pinout for Broadcom cable 05-60007-00, a x8 SFF-8654 to x8 SFF-8654 connection.

Figure 21: Cable 05-60007-00 Drawing and Pinout

	REVISIONS									
REV	ECO	DESCRIPTION	DATE	APPROVED						
001		PRELIMINARY RELEASE	0 I MAY I 8							
002	175141	ADD WIRE GAUGE AND IMPEDANCE TOLERANCE ITEM 2. REVISE MARKING REQUIREMENTS NOTE 3.	2 I NOV I 9							



6	LABEL	LABEL, TOX26MM, WHITE, R2, HF	A/R	SEE NOTE 3
5	TAPE	ACETATE TAPE: W=1 INCH	A/R	
4	SLEEVING	EXPANDO TUBE: OD=10MM, GREEN, VW-I, HF	A/R	
3	WIRE	ULIO61, STRANDED	A/R	SEE NOTE 4
2	CABLE	SAS CABLE: UL20744, 28-32AWG, 92.5Ω ±10% DIFF, SAS4, PCIE GEN 4, VW-1, NATURAL, HF	A/R	
	CONNECTOR	SFF-8654, 74P, STRAIGHT, X8, STANDARD, SAS 4.0, PCIE GEN 4	2	SEE NOTE 4
ITEM	CALLOUT	DESCRIPTION	QUANTITY	NOTES

APPROVALS DRAME BY	DATE 15FEB18	UNLESS OTHERWISE DIMENSIONS ARE IN TOLERANCES: X.X ± 0.2 MM	SPECIFIED: MILLIMETERS.	@ BROADCOM*					
APPROVED BY O I MAY I B  Company Confidential & Copyright Broadcam Limited All Rights Reserved Any copy is an uncontrolled copy.		X.XX ± 0.15 ) X.XXX ± 0.050 ANGLES ± 1		TITLE	CABLE, SFF-8654 TO SFF-8654, X8, STRAIGHT, IM				
The possessor is responsible for a document's revision is current possessor is responsible for re documents from their poin	2.000	⊕€	SIZE D	5067-6869	002	SHEET			

CONNECTOR SIGNAL	CONNECTOR	PIN/PAD		CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE	NOTES
GND	\$0	Al	_	P0	ВІ	GND	ITEM 3	
Rx0+	1	A2	₩	-	B2	T x 0 +	ITEM 3	
Rx0-	1	A3	H	-	B3	T x 0 -	ITEM 3	
GND	1	A4	1 <del>.X</del> —		B4	GND	ITEM 3	
RxI+	1	A5	Н—	-	B5	T x I +	ITEM 3	
RxI-		A6	H	-	B6	TxI-	ITEM 3	
GND	1	A7	1ν_	-	B7	GND	ITEM 3	
SB7A	1	A8	$\vdash$	-	B8	SB7A	ITEM 4	NOTE I
SB4A	1	A9	<b>├</b>	-	B9	SB4A	ITEM 4	NOTE I
GND/SB3A	1	A10	$\vdash$	-	BIO	GND/SB3A	ITEM 3	
SBA+	1	AII	1/1	-	BII	SBA+	ITEM 3	
SBA-	1	A12	11 —		B12	SBA-	ITEM 3	
GND	1	AI3	1-X-		B13	GND	ITEM 3	
R x 2+	1	AI4	1//	-	B14	Tx2+	ITEM 3	
Rx2-	1	A15	11 /	-	B15	Tx2-	ITEM 3	
GND	1	A16	1-X-	-	B16	GND	ITEM 3	
Rx3+	1	A17	1//─	-	B17	Tx3+	ITEM 3	
Rx3-	1	A18	₩	-	B18	Tx3-	ITEM 3	
GND		AI9	H	-	B19	GND	ITEM 3	
Rx4+	1	A20	1⊬1—	-	B20	T x 4 +	ITEM 3	
R x 4 -	1	A21	₩		B21	T x 4 -	ITEM 3	
GND	1	A22	1 <del>.X</del> —	-	B22	GND	ITEM 3	
Rx5+	1	A23	1/1	-	B23	Tx5+	ITEM 3	
Rx5-	1	A24	₩	-	B24	Tx5-	ITEM 3	
GND	1	A25	1		B25	GND	ITEM 3	
SB7B	1	A26	}—	-	B26	SB7B	ITEM 4	NOTE I
SB4B		A27	-	-	B27	SB4B	ITEM 4	NOTE I
GND/SB3B	1	A28	-	-	B28	GND/SB3B	ITEM 3	
SBB+	1	A29	₩	-	B29	SBB+	ITEM 3	
SBB -	1	A30	11 /	-	B30	SBB -	ITEM 3	
GND	1	A31	1-X-	-	B31	GND	ITEM 3	
Rx6+	1	A32	1/1	-	B32	Tx6+	ITEM 3	
Rx6-	1	A33	11	-	B33	T x 6 -	ITEM 3	
GND	1	A34	1-X-	1	B34	GND	ITEM 3	
Rx7+	1	A35	₩-	-	B35	T x 7+	ITEM 3	
Rx7-	1	A36	₩-	-	B36	T x 7 -	ITEM 3	
GND	]	A37	$\vdash$	-	B37	GND	ITEM 3	

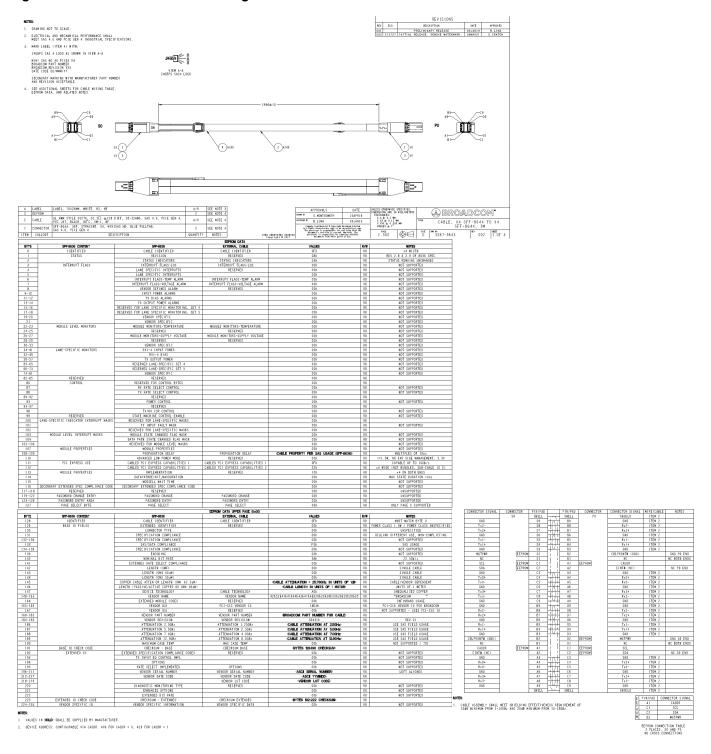
			_					
CONNECTOR SIGNAL	CONNECTOR	PIN/PAD		CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE	NOTES
GND	S0	ВІ	-	P0	Al	GND	ITEM 3	
T x 0 +		B2	H	1	A2	Rx0+	ITEM 3	
T x 0 -		B3	H +	1	A3	R x 0 -	ITEM 3	
GND		84	<del>IX</del>		A4	GND	ITEM 3	
TxI+		B5	H		A5	Rx1+	ITEM 3	
TxI-		B6	Н—		A6	RxI-	ITEM 3	
GND		B7	$\vdash$	1	A7	GND	ITEM 3	
SBOA		B8	$\vdash$	1	A8	SBOA	ITEM 4	NOTE I
SBIA		B9	<del> </del>	-	A 9	SBIA	ITEM 4	NOTE I
GND/SB2A		BIO	-	1	A10	GND/SB2A	ITEM 3	
SB5A		BII	H	1	ALL	SB5A	ITEM 3	
SB6A		B12	₩	-	A12	SB6A	ITEM 3	
GND		B13	<del>  X -</del>		A13	GND	ITEM 3	
Tx2+		B14	Н-	-	A14	Rx2+	ITEM 3	
T x 2 -	1	B15	₩		A15	R x 2 -	ITEM 3	
GND	1	B16	1 <del>.X</del> —		A16	GND	ITEM 3	
Tx3+	1	B17	₩-		A17	Rx3+	ITEM 3	
Tx3-	1	B18	H -		A18	Rx3-	ITEM 3	
GND	1	B19	I <del>X</del> —		A19	GND	ITEM 3	
T x 4+	1	B20	₩-		A20	Rx4+	ITEM 3	
T x 4 -	1	B21	14		A21	Rx4-	ITEM 3	
GND	1	B22	1-X-		A22	GND	ITEM 3	
Tx5+	1	B23	144		A23	Rx5+	ITEM 3	
Tx5-	1	B24	₩		A24	Rx5-	ITEM 3	
GND	1	B25	₩_		A25	GND	ITEM 3	
SBOB	1	B26	1—		A26	SBOB	ITEM 4	NOTE I
SBIB	1	B27	├—		A27	SBIB	ITEM 4	NOTE I
GND/SB2B	1	B28	<del> </del>		A28	GND/SB2B	ITEM 3	
SB5B	1	B29	144		A29	SB5B	ITEM 3	
SB6B	1	B30	14.		A30	SB6B	ITEM 3	
GND	1	B31	1.X.		A31	GND	ITEM 3	
Tx6+	1	B32	144		A32	Rx6+	ITEM 3	
Tx6-	1	B33	14		A33	Rx6-	ITEM 3	
GND	1	B34	1.X.		A34	GND	ITEM 3	
T x 7+	1	B35	1//		A35	Rx7+	ITEM 3	
Tx7-	1	B36	Ш		A36	Rx7-	ITEM 3	
GND	1	B37	LV.		A37	GND	ITEM 3	

NOTES:
I. END TO END RESISTANCE OF INDICATED CONNECTION SHALL BE 750 MILLIOHM MAX.

## Cable 05-60009-00

The following figure shows the drawing and pinout for Broadcom cable 05-60009-00, a x4 SFF-8644 to x4 SFF-8644 connection.

Figure 22: Cable 05-60009-00 Drawing and Pinout



# **Revision History**

### Version 2.5, May 21, 2024

- Updated Board Storage Conditions.
- Added Cable 05-60009-00.

### Version 2.4, January 17, 2024

Added FCC Compliance.

### Version 2.3, October 31, 2023

- Updated the address to 0x4C in Virtual Pin Port Management.
- Removed reference to LED4.

### Version 2.2, September 27, 2023

Updated the 9670-24i adapter connector-to-CSW port associations in Tri-Mode Storage Interface and PCIe (NVMe) Support.

### Version 2.1, August 1, 2023

- Updated the adapter dimensions in Broadcom MegaRAID and HBA Tri-Mode Storage Adapter Characteristics.
- Updated External Adapter Connector Pinout.

### Version 2.0, August 30, 2022

- Updated the resistor values in Sideband Signals.
- Revised Typical Power values in MegaRAID Tri-Mode Storage Adapter Power Supply Requirements and eHBA Tri-Mode Storage Adapter Power Supply Requirements.
- Changed SFF-8644 instances to SFF-8674.

### Preliminary, Version 1.1, June 13, 2022

- Revised the NVMe SSD count in MegaRAID 9670W-16i, 9670-24i, and 9660-16i Adapter RAID Features.
- Updated the 05-60006-00 description in Storage Interface Cabling.
- Added Overtemperature Behavior.
- Updated the Adapter Marks and Certifications table in Marks, Certifications, and Compliance.
- Added the eHBA 9600-8i8e adapter.
- Renamed HBA to eHBA.

### Preliminary, Version 1.0, March 8, 2022

- Updated the 9670-24i and 9600-24i adapters in the NVMe Device or PCle Switch Direct-Attach Options Supported for Each Adapter table in PCle (NVMe) Support.
- Updated the following tables in PCIe (NVMe) Support:

- 9670-24i Adapter PCIe Topology Configuration Combinations
- 9600-24i Adapter PCIe Topology Configuration Combinations
- Updated External Adapter Connector Pinout.
- Added board layout images.
- Updated Tri-Mode Storage Adapter Power Supply Requirements.
- Updated the values regarding the CacheVault power module in MegaRAID Tri-Mode Storage Adapter Power Supply Requirements.

### Advance, Version 0.2, March 19, 2021

- Changed the 9660-16i cache memory description in the MegaRAID Tri-Mode Storage Adapter Features table in Overview.
- Updated the drive descriptions in the HBA Tri-Mode Storage Adapter Features table in Overview.
- Revised eHBA 9600 Adapter Features.
- Added a note to PCle (NVMe) Support.
- Added Adapter Security.
- Added the MegaRAID 9670-24i adapter.

### Advance, Version 0.1, April 27, 2020

Initial document release.

