

AES6800+BS/BD/CS/CD

1x4 or 1x9, AES/EBU, Balanced or Unbalanced

The AES6800+ is a differential input, nine output, AES/EBU digital audio distribution amplifier (DA) for use in balanced and unbalanced installations within the 6800+™ modular core processing platform.

It features cable equalization and data reclocking (sampling frequency of 32 and 192 kHz), making it ideal for AES distribution applications.

Features

- Balanced and unbalanced version, compliant with SMPTE 270M and AES-3id-2001
- Manual or automatic equalization modes
- Bypass mode for non-AES (non bi-phase encoded) signals up to 30 MHz 50% duty cycle
- Data reclocking provides jitter reduction
- LED indication of input lock and other important errors
- Housed in 6800+™ frame
- Full CCST™ integration with remote monitoring capability
- Card-edge LEDs for indication of loss of lock or signal quality errors

Specifications

Specifications and designs are subject to change without notice

INPUTS	BALANCED I/O	COAXIAL I/O
Type	Balanced, transformer coupled	AC coupled
Quantity (input signals)	1	1
Connector (signals)	3-Pin audio WECO	BNC
Impedance	110 ohms	75 ohms
Return Loss	N/A	>30 dB
AES Frame Rates	30 to 192 kHz	30 to 192 kHz
Signal Amplitude	0.2 to 7 V pk-pk	0.1 V to 2 V pk-pk
Input Cable Length	0 to 300 m twisted pair (Belden 8451 or equivalent)	0 to 600 m coaxial (Belden 8281 or equivalent)

OUTPUTS	BALANCED I/O	COAXIAL I/O
Type	Balanced, transformer coupled	Unbalanced
Quantity	4,9	4,9
Connector	3-Pin audio WECO	BNC
Impedance	110 ohms	75 ohms
Return Loss	N/A	>35 dB
Signal Amplitude	5 V pk-pk \pm 1 V into 110 ohms load	1 V pk-pk \pm 10% into 75 ohms load
DC Offset	0 V \pm 50 mV	0 V \pm 50 mV
Rise and Fall Time	5 to 30 ns	30 to 44 ns
Jitter	<5 ns	<5 ns
Propagation Delay	<600 ns @ 48 kHz	<600 ns @ 48 kHz



Ordering Information

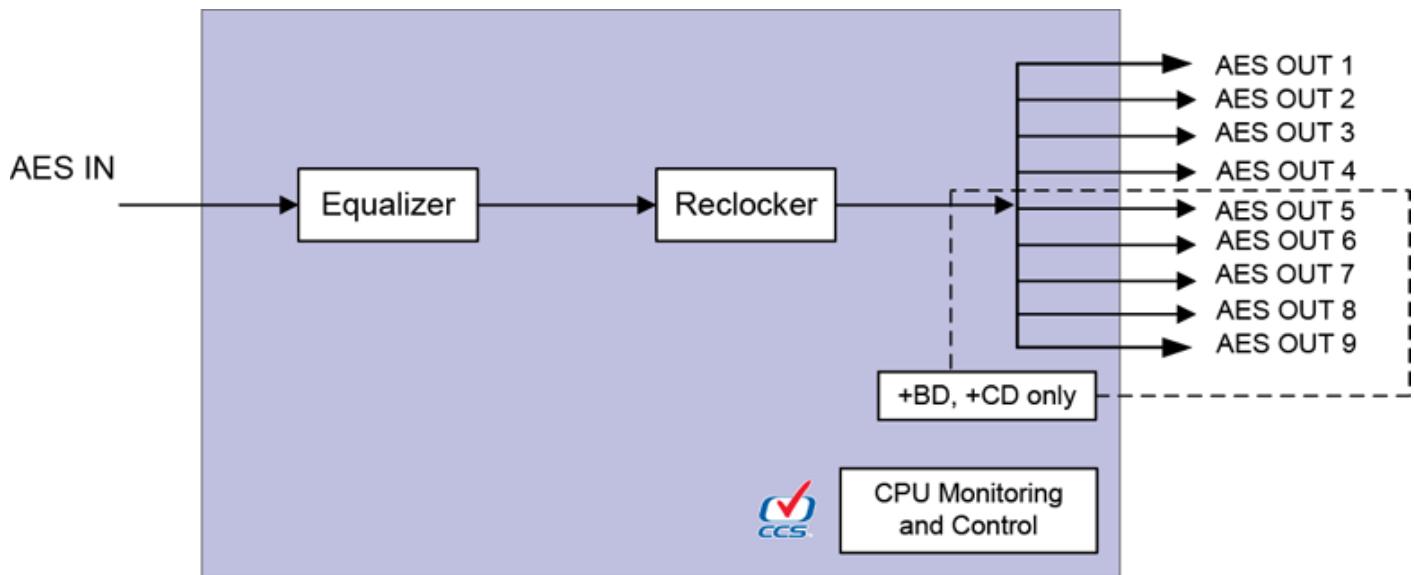
AES6800+BS	1x4 DA, AES/EBU balanced, 1 slot
AES6800+BD	1x9 DA, AES/EBU balanced, 2 slots
AES6800+CS	1x4 DA, AES/EBU unbalanced, 1 slot
AES6800+CD	1x9 DA, AES/EBU unbalanced, 2 slots

Images/Diagrams

Selenio 6800+™



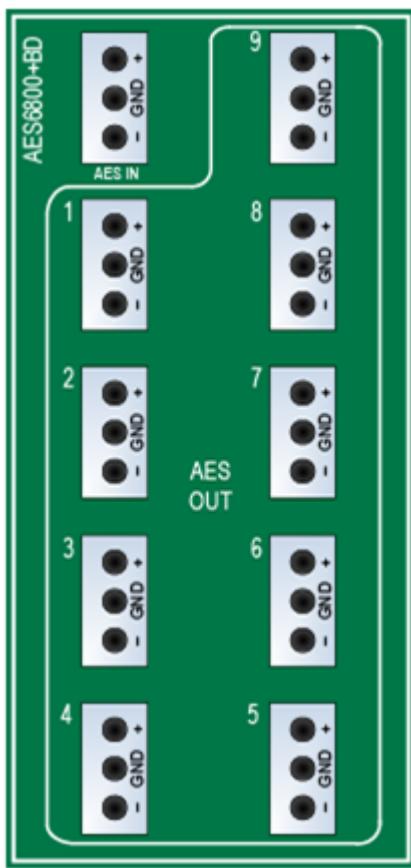
Block Diagram



AES6800+BS Back Module



AES6800+BD Back Module



AES6800+CS Back Module

